MITSUBISHI

Type A1SCPUC24-R2/A1SHCPU/ A2SHCPU(S1)/A2ASCPU(S1/S30)/ A2USHCPU-S1

Mitsubishi Programmable Controller

User's Manual

(Hardware)

Thank you for purchasing the Mitsubishi programmable controller MELSEC-A series.

Prior to use, please read both this and relevant manuals thoroughly to fully understand the product.



MODEL	A1S/A2S/A2ASCPU (H/W)-U-E	
MODEL CODE	13JE59	
IB(NA)-66468-R(1112)MEE		

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SAFETY PRECAUTIONS •

(Read these precautions before using this product.)

Before using this product, please read this manual and the relevant manuals carefully and pay full attention to safety to handle the product correctly.

In this manual, the safety precautions are classified into two levels: "\.\ WARNING" and "\.\.\ CAUTION".



Indicates that incorrect handling may cause hazardous conditions, resulting in death or severe injury.



Indicates that incorrect handling may cause hazardous conditions, resulting in minor or moderate injury or property damage.

Under some circumstances, failure to observe the precautions given under "\(\hat{\capacita}\) CAUTION" may lead to serious consequences.

Observe the precautions of both levels because they are important for personal and system safety.

Make sure that the end users read this manual and then keep the manual in a safe place for future reference.

[DESIGN PRECAUTIONS]

*∧***WARNING**

- Create a safety circuit outside the programmable controller to ensure the whole system will operate safely even if an external power failure or a programmable controller failure occurs.
 - Otherwise, incorrect output or malfunction may cause an accident.
 - (1) For an emergency stop circuit, protection circuit and interlock circuit that is designed for incompatible actions such as forward/reverse rotation or for damage prevention such as the upper/lower limit setting in positioning, any of them must be created outside the programmable controller.
 - Install the emergency stop switch outside the controlpanel so that workers can operate it easily.

[DESIGN PRECAUTIONS]

MWARNING

- (2) When the programmable controller detects the following error conditions, it stops the operation and turn off all the outputs.
 - The overcurrent protection device or overvoltage protection device of the power supply module is activated.
 - The programmable controller CPU detects an error such as a watchdog timer error by the self-diagnostics function.

In the case of an error of a part such as an I/O control part that cannot be detected by the programmable controller CPU, all the outputs may turn on. In order to make all machines operate safely in such a case, set up a fail-safe circuit or a specific mechanism outside the programmable controller.

Refer to "LOADING AND INSTALLATION" in this manual for example fail safe circuits.

- (3) Depending on the failure of the output module's relay or transistor, the output status may remain ON or OFF incorrectly. For output signals that may lead to a serious accident, create an external monitoring circuit.
- If load current more than the rating or overcurrent due to a short circuit in the load has flowed in the output module for a long time, it may cause a fire and smoke. Provide an external safety device such as a fuse.
- Design a circuit so that the external power will be supplied after power-up of the programmable controller.
 - Activating the external power supply prior to the programmable controller may result in an accident due to incorrect output or malfunction.
- For the operation status of each station at a communication error in data link, refer to the respective data link manual.
 - The communication error may result in an accident due to incorrect output or malfunction.

[DESIGN PRECAUTIONS]

↑ WARNING

- When controlling a running programmable controller (data modification) by connecting a peripheral device to the CPU module or a PC to a special function module, create an interlock circuit on sequence programs so that the whole system functions safely all the time.
 - Also, before performing any other controls (e.g. program modification, operating status change (status control)), read the manual carefully and ensure the safety.
 - In these controls, especially the one from an external device to a programmable controller in a remote location, some programmable controller side problem may not be resolved immediately due to failure of data communications.
 - To prevent this, create an interlock circuit on sequence programs and establish corrective procedures for communication failure between the external device and the programmable controller CPU.
- When setting up the system, do not allow any empty slot on the base unit.
 If any slot is left empty, be sure to use a blank cover (A1SG60) or a dummy module (A1SG62) for it.
 - When using the extension base unit, A1S52B(S1), A1S55B(S1) or A1S58B(S1), attach the included dustproof cover to the module in slot 0. Otherwise, internal parts of the module may be flied in the short circuit test or when an overcurrent or overvoltage is accidentally applied to external I/O section.

↑ CAUTION

- Do not install the control lines or communication cables together with the main circuit or power lines, or bring them close to each other.
 Keep a distance of 100mm (3.94inch) or more between them.
 Failure to do so may cause malfunctions due to noise.
- When an output module is used to control the lamp load, heater, solenoid valve, etc., a large current (ten times larger than the normal one) may flow at the time that the output status changes from OFF to ON. Take some preventive measures such as replacing the output module with the one of a suitable current rating.
- Time from when the CPU module is powered on or is reset to when it enters in RUN status dependson the system configuration, parameter settings, and program size.
 - Design the program so that the entire system will always operate safely, regardless of the time.

[INSTALLATION PRECAUTIONS]

A CAUTION

- Use the programmable controller under the environment specified in the user's manual.
 - Otherwise, it may cause electric shocks, fires, malfunctions, product deterioration or damage.
- Insert the module fixing projection into the fixing hole in the base unit and then tighten the module mounting screw within the specified torque.
 When no screw is tightened, even if the module is installed correctly, it may cause malfunctions, a failure or a drop of the module.

Tightening the screw excessively may damage the screw and/or the module, resulting in a drop of the module, a short circuit or malfunctions.

- Connect the extension cable to the connector of the base unit or module.
 Check the cable for incomplete connection after connecting it.
 Poor electrical contact may cause incorrect inputs and/or outputs.
- Correctly connect the memory cassette installation connector to the memory cassette. After installation, be sure that the connection is not loose.
 A poor connection could cause an operation failure.
- Be sure to shut off all phases of the external power supply used by the system before mounting or removing the module. Failure to do so may damage the module.
- Do not directly touch the conductive part or electronic components of the module.
 - Doing so may cause malfunctions or a failure of the module.

[WIRING PRECAUTIONS]

AWARNING

- Be sure to shut off all phases of the external power supply used by the system before wiring.
 - Failure to do so may result in an electric shock or damage of the product.
- Before energizing and operating the system after wiring, be sure to attach the terminal cover supplied with the product.
 - Failure to do so may cause an electric shock.

⚠ CAUTION

- Always ground the FG and LG terminals to the protective ground conductor.
 Failure to do so may cause an electric shock or malfunctions.
- Wire the module correctly after confirming the rated voltage and terminal layout.
- Connecting a power supply of a different voltage rating or incorrect wiring may cause a fire or failure.
- Do not connect multiple power supply modules to one module in parallel.
 The power supply modules may be heated, resulting in a fire or failure.
- Press, crimp or properly solder the connector for external connection with the specified tool.
 - Incomplete connection may cause a short circuit, fire or malfunctions.
- Tighten terminal screws within the specified torque range. If the screw is too loose, it may cause a short circuit, fire or malfunctions.
 If too tight, it may damage the screw and/or the module, resulting in a short circuit or malfunctions.
- Carefully prevent foreign matter such as dust or wire chips from entering the module.
 - Failure to do so may cause a fire, failure or malfunctions.
- Install our programmable controller in a control panel for use.
 Wire the main power supply to the power supply module installed in a control panel through a distribution terminal block.
 - Furthermore, the wiring and replacement of a power supply module have to be performed by a maintenance worker who acquainted with shock protection.
 - (For the wiring methods, refer to Type A1SCPUC24-R2/A1SHCPU/A2SHCPU(S1)/A2ASCPU(S1/S30)/A2USHCPU-S1 User's Manual (Hardware))

[STARTUP AND MAINTENANCE PRECAUTIONS]

↑ WARNING

- Do not touch any terminal during power distribution.
 Doing so may cause an electric shock.
- Properly connect batteries. Do not charge, disassemble, heat or throw them
 into the fire and do not make them short-circuited and soldered. Incorrect
 battery handling may cause personal injuries or a fire due to exothermic heat,
 burst and/or ignition.
- Be sure to shut off all phases of the external power supply used by the system before cleaning or retightening the terminal screws or module mounting screws.

Failure to do so may result in an electric shock.

If they are too loose, it may cause a short circuit or malfunctions.

If too tight, it may cause damage to the screws and/or module, resulting in an accidental drop of the module, short circuit or malfunctions.

⚠ CAUTION

- When performing online operations (especially, program modification, forced output or operating status change) by connecting a peripheral device to the running CPU module, read the manual carefully and ensure the safety.
 Incorrect operation will cause mechanical damage or accidents.
- Do not disassemble or modify each of modules.
 Doing so may cause failure, malfunctions, personal injuries and/or a fire.
- When using a wireless communication device such as a mobile phone, keep a distance of 25cm (9.84inch) or more from the programmable controller in all directions.

Failure to do so may cause malfunctions.

- Be sure to shut off all phases of the external power supply used by the system before mounting or removing the module.
 Failure to do so may result in failure or malfunctions of the module.
- Do not drop or apply any impact to the battery.
 Doing so may damage the battery, resulting in electrolyte spillage inside the battery.
 - If any impact has been applied, discard the battery and never use it.
- Do not install/remove the terminal block more than 50 times after the first use of the product. (IEC 61131-2 compliant)
- Before handling modules, touch a grounded metal object to discharge the static electricity from the human body.

Failure to do so may cause failure or malfunctions of the module.

[DISPOSAL PRECAUTIONS]

⚠ CAUTION

- When disposing of the product, treat it as an industrial waste.
 When disposing of batteries, separate them from other wastes according to the local regulations.
 - (For details of the Battery Directive in EU member states, refer to User's manual for the CPU module used.)

[TRANSPORTATION PRECAUTIONS]

A CAUTION

 When transporting lithium batteries, make sure to treat them based on the transportation regulations. (Refer to Appendix 2 for details of the relevant models.)

●安全注意事项●

(使用之前请务必阅读)

在使用本产品之前,应仔细阅读本手册以及本手册中所介绍的相关手册,同时在充分注意安全的前提下正确操作。

在"安全注意事项"中,安全注意事项被分为"⚠警告"和"⚠注意"两个等级。

⚠ 警告

表示错误操作可能造成危险后果,导致死亡或重伤事故。

⚠ 注意

表示错误操作可能造成危险后果,导致中度伤害、轻伤 或财产损失。

此外,根据情况不同,即使标注为"允注意"的事项也有可能会引发严重后果。 这两个等级的注意事项记载的均为重要内容,请务必遵守。 请妥善保管本手册以备需要时取阅,并将本手册交给最终用户。

【设计注意事项】

⚠ 警告

- 应在可编程控制器外部设置一个安全电路,以保证整个系统在外部电源异常或可编程控制器本体故障时也能安全运行。否则可能由于误输出、误动作而导致事故发生。
 - (1) 应在可编程控制器外部配置紧急停止电路、保护电路、正转/反转等相反动作的互锁电路、定位的上限/下限等防止机械破损的互锁电路等。紧急停止开关应设置在控制盘外以便于作业人员操作。
 - (2) 可编程控制器在检测到下列异常状态时,会停止运算并 0FF 所有输出。
 - 电源模块的过电流保护装置或过电压保护装置工作时。
 - •可编程控制器 CPU 通过看门狗定时器出错等自诊断功能检测到异常时。
 - 此外,出现可编程控制器 CPU 所无法检测的输入输出控制部分等的异常时,所有输出可能会为 ON。此时,应在可编程控制器外部配置一个失效安全电路或装置,以保证机械安全运行。关于失效安全电路的示例,请参照本手册的"实际安装与设置"。
 - (3)输出模块的继电器以及晶体管等的故障可能会导致输出保持 0N 状态或 OFF 状态。对于可能导致重大事故发生的输出信号,应在外部设置监视电路。
- 输出模块因为超过额定的负载电流或负载短路等导致长时间过电流时,可能会导致冒烟、火灾,因此请在外部设置保险丝等安全电路。
- 应配置成在可编程控制器本体电源接通后再接通外部供应电源的电路。如果外部供应电源先接通,可能由于误输出或误动作而导致事故发生。
- 关于数据链接出现通信异常时各站的动作状态,请参照各数据链接的手册。 否则可能由于误输出、误动作而导致事故发生。

【设计注意事项】

♠ 警告

在 CPU 模块上连接外围设备,或在特殊功能模块上连接计算机等以对运行中的可编程控制器进行控制(数据更改)前,应在顺控程序上配置互锁电路,以保证整个系统始终能安全运行。

此外,对运行中的可编程控制器进行其他控制(程序更改、运行状态更改(状态控制))前,应仔细阅读相关手册,在充分确认安全的基础上进行操作。

特别是在通过外部设备对远程的可编程控制器进行上述控制时,可能会因为数据通信异常而导致无法立即对可编程控制器侧的故障做出反应。

在顺控程序上配置互锁电路的同时,应在外部设备与可编程控制器 CPU 之间确定发生数据通信异常时的系统的处理方法。

● 配置系统时,请勿在基板上设置空余插槽。 出现空余插槽时,必须使用空槽盖板 (A1SG60)、虚拟模块 (A1SG62)。 此外,使用扩展基板 A1S52B、A1S55B、A1S58B 时,必须在 0 号插槽的模块 上安装附带的防尘盖板。

立了短路试验时,或误对外部输入输出部施加过电流或过电压时,模块的内部部件可能会四处飞散。

【设计注意事项】

♠ 注意

- 请勿将控制线及通信电缆与主电路及动力线等捆扎在一起或相互靠得太近。 应相距大约100mm以上距离。
 因为噪声有可能导致误动作。
- 通过输出模块控制指示灯负载、加热器、电磁阀等时,输出 0FF → 0N 时可能会有大电流(通常的 10 倍左右)流过,因此请采取改用具有足够额定电流的输出模块等措施。
- CPU 模块的电源关闭→接通或复位时,CPU 模块变为 RUN 状态为止的时间会 因系统配置、参数设置、程序容量等而变动。
 - 应通过设计使得在变为 RUN 状态为止的时间发生变动时也能保证整个系统安全运行。

【安装注意事项】

企注意

- 应在本手册记载的一般规格环境下使用可编程控制器。 如果在一般规格范围以外的环境中使用可编程控制器,可能导致触电、火 灾、误动作、产品损坏或性能劣化。
- 请将模块下部的模块固定用凸起部切实插入基板的固定孔后,以规定的扭矩拧紧模块安装螺栓。如果模块未正确安装并以螺栓固定,有可能造成误动作、故障或掉落。

如果螺栓拧得过紧,有可能造成螺栓及模块破损从而导致掉落、短路或误动作。

- 扩展电缆应切实安装到基板的扩展电缆用接口上。 安装后应检查有无浮起。
 - 因为接触不良有可能导致误输入、误输出。
- 存储卡盒应按入存储卡盒安装用接口中并切实安装。 安装后应检查有无浮起。 因为接触不良有可能导致误动作。
- 在拆装模块时,必须将系统使用的外部供应电源全部断开后再进行操作。 如果未全部断开,有可能导致产品损坏。
- 请勿直接触碰模块的导电部分及电子部件。 否则可能导致模块误动作、故障。

【配线注意事项】

▲ 警告

- 在配线作业等时,必须将系统使用的外部供应电源全部断开后再进行操作。如果未全部断开,有可能导致触电或产品损坏。
- 配线作业结束后进行通电、运行时,必须安装产品附带的端子盖板。如果未安装端子盖板,有可能导致触电。

♠ 注意

- 必须将 FG 端子及 LG 端子与可编程控制器的专用接地线连接。 否则有可能导致触电、误动作。
- 进行模块配线作业时,应在确认产品的额定电压及端子排列的基础上正确进行操作。如果连接了与额定值不符的电源或配线错误,可能导致火灾或故障。
- 请勿将多个电源模块的输出并联。
 - 否则电源模块会过热,可能导致火灾或故障。
- 外部连接用接口应使用生产厂商指定的工具进行压装、压接或正确焊接。如果连接不牢固,可能导致短路、火灾或误动作。
- 应在规定的扭矩范围内拧紧端子螺栓。
 - 如果端子螺栓拧得过松,有可能导致短路、火灾或误动作。 如果端子螺栓拧得过紧,有可能造成螺栓及模块破损从而导致掉落、短路 或误动作。
- 应注意防止切屑及配线头等异物掉入模块内。
 - 否则有可能导致火灾、故障或误动作。
- 本公司的可编程控制器应设置在控制盘内使用。
 - 与设置在控制盘内的可编程控制器电源模块之间的主电源配线应经由中继端子排进行。
 - 此外,电源模块的更换和配线作业应由接受过充分的触电防护教育的维护作业人员进行。
 - 配线方法请参照4.3.4项。

【启动/维护注意事项】

♠ 警告

- 在通电状态下请勿触摸端子。
 - 否则可能导致触电。
- 应正确连接电池。
 - 请勿进行充电、拆解、加热、投入火中、短接、焊接等。
 - 如果错误使用电池,有可能因为电池发热、破裂或起火等而导致人身伤害 或火灾。
- 在清洁模块或重新紧固端子螺栓、模块安装螺栓时,必须将系统使用的外 部供应电源全部断开后再进行操作。

如果未全部断开,有可能导致触电。

如果端子螺栓拧得过松,有可能导致短路、误动作。

如果螺栓拧得过紧,有可能造成螺栓及模块破损从而导致掉落、短路或误 动作。

⚠ 注意

- 把外围设备连接到运行中的 CPU 模块上进行在线操作(特别是程序更改、 强制输出、运行状态的更改)前,应仔细阅读手册,在充分确认安全的基 础上进行操作。
 - 否则操作错误有可能导致机械破损或事故发生。
- 请勿拆解或改造各模块。
 - 否则可能导致故障、误动作、人身伤害或火灾。
- 便携电话或 PHS 等无线通信设备应在距离可编程控制器本体(各个方向) 25cm以上的地方使用。
 - 否则可能导致误动作。
- 在拆装模块时,必须将系统使用的外部供应电源全部断开后再进行操作。 如果未全部断开,有可能导致模块故障或误动作。
- 请勿使模块中安装的电池掉落或受到撞击。
 - 掉落或受到撞击可能会导致电池破损,造成电池内部发生漏液。
 - 掉落或受到撞击的电池不可使用, 应予以报废。
- 产品投入使用后,端子排的拆装次数不应超过50次。 (根据 IEC61131-2 规范)
- 在触碰模块之前,必须先触碰已接地的金属等,释放掉人体等所携带的静 电。
 - 如果不释放掉静电,有可能导致模块故障或误动作。

【报废处理注意事项】

⚠ 注意

本产品报废时,应当作工业废物处理。电池报废时应根据各地区所制定的法律法规分别进行。(关于欧盟成员国的电池管制的详细内容,请参照所使用的CPU模块的用户手册。)

【运输注意事项】

⚠ 注意

 在运输含锂电池时,应根据运输管制进行处理。(管制对象机种的详细内容 请参照附,2。)

CONDITIONS OF USE FOR THE PRODUCT

- Mitsubishi programmable controller ("the PRODUCT") shall be used in conditions:
 - i) where any problem, fault or failure occurring in the PRODUCT, if any, shall not lead to any major or serious accident; and
 - ii) where the backup and fail-safe function are systematically or automatically provided outside of the PRODUCT for the case of any problem, fault or failure occurring in the PRODUCT.
- (2) The PRODUCT has been designed and manufactured for the purpose of being used in general industries.

MITSUBISHI SHALL HAVE NO RESPONSIBILITY OR LIABILITY (INCLUDING, BUT NOT LIMITED TO ANY AND ALL RESPONSIBILITY OR LIABILITY BASED ON CONTRACT, WARRANTY, TORT, PRODUCT LIABILITY) FOR ANY INJURY OR DEATH TO PERSONS OR LOSS OR DAMAGE TO PROPERTY CAUSED BY the PRODUCT THAT ARE OPERATED OR USED IN APPLICATION NOT INTENDED OR EXCLUDED BY INSTRUCTIONS, PRECAUTIONS, OR WARNING CONTAINED IN MITSUBISHI'S USER, INSTRUCTION AND/OR SAFETY MANUALS, TECHNICAL BULLETINS AND GUIDELINES FOR THE PRODUCT

("Prohibited Application")

Prohibited Applications include, but not limited to, the use of the PRODUCT in:

- Nuclear Power Plants and any other power plants operated by Power companies, and/or any other cases in which the public could be affected if any problem or fault occurs in the PRODUCT.
- Railway companies or Public service purposes, and/or any other cases in which establishment of a special quality assurance system is required by the Purchaser or End User.
- Aircraft or Aerospace, Medical applications, Train equipment, transport
 equipment such as Elevator and Escalator, Incineration and Fuel
 devices, Vehicles, Manned transportation, Equipment for Recreation
 and Amusement, and Safety devices, handling of Nuclear or
 Hazardous Materials or Chemicals, Mining and Drilling, and/or other
 applications where there is a significant risk of injury to the public or
 property.

Notwithstanding the above, restrictions Mitsubishi may in its sole discretion, authorize use of the PRODUCT in one or more of the Prohibited Applications, provided that the usage of the PRODUCT is limited only for the specific applications agreed to by Mitsubishi and provided further that no special quality assurance or fail-safe, redundant or other safety features which exceed the general specifications of the PRODUCTs are required. For details, please contact the Mitsubishi representative in your region.

REVISIONS

*The manual number is given on the bottom right of the cover.

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This manual describes cautions on handling, connection to I/O modules, and error codes of A2ASCPU(S1/S30), and A1SHCPU, A2SHCPU(S1), A1SCPUC24-R2 (hereafter called "the CPU").

Manuals

The following manuals are related to this product. Refer to the following manuals when necessary.

Detailed manuals

Manual Name	Manual No. (Model Code)
Type A1S/A1SC24-R2/A2SCPU(S1) User's manual This manual describes the specifications and functions of A1S, A1SC24-R2 and A2SCPU(S1), and specifications etc. of the memory cassettes, the power supply module and extension base unit. (Sold separately)	IB-66320 (13J672)
Type A2ASCPU(S1/S30) User's manual This manual describes the specifications and functions of A2ASCPU(S1/S30) and the specifications of the memory cassettes, the power supply modules and extension base units that can be used with it. (Sold separately)	IB-66455
Type A1SJH(S8)/A1SH/A2SHCPU (S1) User's Manual This manual describes the specifications and functions of A1SJHCPU(S8), A1SHCPU, and A2SHCPU(S1) and the specifications of the memory cassettes, the power supply modules and extension base units that can be used with it. (Sold separately)	IB-66779 (13JL22)
Computer Link Module (Comms. link func./ Print func.) User's Manual This manual describes communication between the A1SCPUC24-R2 and external devices using the dedicated protocol, no protocol, and bidirectional modes, and the settings, wiring, programming, troubleshooting, etc., for this module. (Sold separately)	SH-3511 (13JE77)
Computer Link Module Guidebook This manual gives the basic information required to execute data communication with external devices (computers, for example), in each mode of the computer link function. (Sold separately)	SH-3510 (13JE76)

Related manuals

Manual Name	Manual No. (Model Code)
Type ACPU/QCPU-A (A Mode) Programming Manual (Fundamentals) Describes programming methods necessary for creating programs, device names, parameters, program types, memory area configuration, and so on. (Optional)	IB-66249 (13J740)
Type ACPU/QCPU-A (A Mode) Programming Manual (Common Instructions) Describes how to use the sequence instruction, basic instructions, applied instructions and microcomputer programs. (Optional)	IB-66250 (13J741)
Type AnSHCPU/AnACPU/AnUCPU/QCPU-A (A Mode) Programming Manual (Dedicated Instructions) Describes instructions that have been expanded for A1SJHCPU/ A1SHCPU/A2SHCPU(S1). (Optional)	IB-66251 (13J742)
AnS Module type I/O User's Manual Describes the specification of the compact building block type I/O module. (Optional)	IB-66541 (13JE81)

USER PRECAUTONS

Precautions when using the AnS series

For a new CPU module, which has never used before, the contents of built-in RAM and device data are undefined.

Make sure to clear the built-in RAM memory (PC memory all clear) in the CPU module by peripheral devices and operate latch clear by RUN/STOP key switches.

Precautions for battery

- The operation after a battery is unmounted and the programmable controller is stored.
 - When reoperating after a battery is uncounted and the programmable controller is stored, the contents of built-in RAM and device data may be undefined. For this reason, make sure to clear the built-in RAM memory (PC memory all clear) in the CPU module by peripheral devices and operate latch clear by RUN/STOP key switch before start the operation again.
- (2) If a battery exceeded its guaranteed life is stored and reoperated. If a battery exceeded its guaranteed life is stored and reoperated, the contents of built-in RAM and device data may be undefined. For this reason, make sure to clear the built-in RAM memory (PC memory all clear) in the CPU module by peripheral devices and operate latch clear by RUN/STOP key switches before start the operation again.
 - After the built-in RAM clear and latch clear of the CPU module, write the backed-up memory contents to the CPU module before saving.

POINT

Make sure to back-up each memory contents before storing the programmable controller.

- Refer to the following manuals for details of built-in RAM clear (programmable controller memory all clear) by peripheral devices.
 - · GX Developer Operating Manual
 - · A6GPP/A6PHP Operating Manual
 - SW□SRX/SW□NX/SW□IVD-GPPA Operating Manual

Refer to Section 4.6 for latch clear operation by RUN/STOP key switch of the CPU module.

1. SPECIFICATIONS

1.1 SPECIFICATIONS

Table 1.1 General specification

Item	Specifications					
Operating ambient temperature	0 to 55 °C					
Storage ambient temperature			-20 to 75	5 °C		
Operating ambient humidity		10 to	00%PH por	n-condensing		
Storage ambient humidity		10 10	90 /61(11, 1101	i-condensing		
			Frequency	Constant acceleration	Half amplitude	Sweep count
	Compliant with	Under	5 to 9Hz	-	3.5mm	10 times
Vibration JIS B 3502 resistance*4 and IEC	intermittent vibration	9 to 150Hz	9.8m/s ²	-	each in X, Y, Z directions	
	61131-2	Under	5 to 9Hz	-	1.75mm	
		continuous vibration	9 to 150Hz	4.9m/s ²	-	-
Shock resistance		Compliant with JIS B 3502 and IEC 61131-2 (147 m/s ² , 3 times each in 3 directions X, Y, Z)				
Operating atmosphere			No corrosive	e gases		
Operating altitude *3		0 to 2000m				
Installation location	Inside a control panel					
Overvoltage category *1	II or less					
Pollution degree *2	2 or less					
Equipment class			Class			

^{*1} This indicates the section of the power supply to which the equipment is assumed to be connected between the public electrical power distribution network and the machinery within premises.

Category II applies to equipment for which electrical power is supplied from fixed facilities. The surge voltage withstand level for up to the rated voltage of 300V is 2500V.

- *2 This index indicates the degree to which conductive material is generated in terms of the environment in which the equipment is used. Pollution level 2 is when only non-conductive pollution occurs. A temporary conductivity caused by condensing must be expected occasionally.
- *3 Do not use or store the programmable controller under pressure higher than the atmospheric pressure of altitude 0m. Doing so may cause malfunction. When using the programmable controller under pressure, please consult your local Mitsubishi Electric representative.
- *4 When an A series extension base unit (A52B, A55B, A58B, A62B, A66B) is used in the system, the following specifications apply.

	Frequency	Acceleration	Amplitude	Sweep count
Under	10 to 57Hz	-	0.075mm	10 times each in
intermittent vibration	57 to 150Hz	9.8m/s ²	-	X, Y, Z directions
Under	10 to 57Hz	-	0.035mm	
continuous vibration	57 to 150Hz	4.9m/s ²	-	-

2. PERFORMANCE SPECIFICATIONS

2.1 Performance Specifications

2.1.1 A1SCPUC24-R2 Module Performance Specifications

The memory capacity, device performances and other specifications of A1SCPUC24-R2 module are provided below.

Table 2.1 Performance specifications

	Type	4400PU004 P0	
Item		A1SCPUC24-R2	
Control system		Repeated operation (using stored program)	
I/O control me	ethod	Refresh mode/Direct mode selectable	
Programming language		Language dedicated to sequence control. Relay symbol type and logic symbolic language, MELSAP-II (SFC) (for A1SCPU)	
Processing sp		Direct : 1.0 to 2.3 μs/step	
(sequence ins		Refresh : 1.0 μs/step	
Number of I/C		256 points (32 points are used for the computer link function)	
Watchdog tim	er (WDT)	10 to 2000 ms	
Memory capa RAM)	city *1 (built-in	32k bytes	
Program	Main sequence	Max. 8 k steps	
capacity	Sub sequence	None	
Self-diagnosti	cs functions	Watchdog error monitor, Memory error detection, CPU error detection, I/O error detection, Battery error detection, etc.	
Operation mo error	de at the time of	STOP/CONTINUE	
Start method RUN	at the time of	Initial start (Automatic restart by turning on the "RUN" switch of the CPU after power-ON/recovery from power failure)	
STOP → RUN	N output mode	Output data at time of STOP restored/data output after operation execution	
Clock function		Year, month, day, hour, minute, second (Automatically recognizes leap years.) Accuracy -2.3 to +4.4 s (TYP. +1.8 s)/d at 0 °C -1.1 to +4.4 s (TYP. +2.2 s)/d at 25 °C -9.6 to +2.7 s (TYP2.4 s)/d at 55 °C	
Allowable momentary power interruption time		Depends on power supply module	
Current consu	umption (5 V DC)	0.56 A	
Weight		0.41 kg	
Standard		UL/CSA	

^{*1} The maximum total memory that can be used for parameters, T/C set values, program capacity, file registers, number of comments, sampling trace, and status latch is 32k/64k bytes.

The memory capacity is fixed. No expansion memory is available. Section 4.1.7 shows how to calculate the memory capacity.

2.1.2 AnSHCPU Module Performance Specifications

The memory capacity, device performances and other specifications of AnSHCPU modules are provided below.

Table 2.2 Performance specifications

_				
Type		A1SHCPU	A2SHCPU(S1)	
Control system		Repeated operation (u	sing stored program)	
I/O control method		Refresh mode/Direct i	node selectable	
Programming lang	uage	Language dedicated to sequence control. Relay symbol type and logic symbolic language, MELSAP-II(SFC)		
Processing speed		Direct: 0.33 to 2.2 μs/s	step Direct: 0.25 to 2.15 μs/step	
(sequence instruct	ion)	Refresh: 0.33 µs/step	Refresh : 0.25 μs/step	
Number of I/O dev	ice points	2048 *2		
Number of I/O poir	nts	256 points	A2SH: 512 points, A2SH-S1: 1024 points	
Watchdog timer (V	VDT)	10 to 2000 ms		
Memory capacity * (built-in RAM)	1	64k bytes	A2SH: 64 k bytes, A2SH-S1: 192 k bytes	
Program capacity	Main sequence	Max. 8 k steps	A2SH: Max. 14 k steps, A2SH-S1: Max. 30 k steps	
	Sub sequence	Unavailable		
Self-diagnostics fu	nctions	Watchdog error monitor, Memory error detection, CPU error detection, I/O error detection, Battery error detection, etc.		
Operation mode at	the time of error	STOP/CONTINUE		
STOP → RUN out	put mode	Output data at time of STOP restored/data output after operation execution		
Start method at the	e time of RUN	Initial start (Automatic restart by turning on the "RUN" switch of the CPU after power-ON/recovery from power failure)		
Clock function		Year, month, day, hour, minute, second (Automatically recognizes leap years.) Accuracy -3.1 to +5.3 s (TYP. +1.7 s)/d at 0 °C		
Clock function		-3.1 to +5.3 s (11P. +1.7 s)/d at 0 °C -1.6 to +5.3 s (TYP. +2.4 s)/d at 25 °C -9.6 to +3.6 s (TYP2.1 s)/d at 55 °C		
Allowable momentary power failure period		Depends on power su	· · ·	
Current consumpti	on (5 V DC)	0.3 A	0.4 A	
Weight		0.33 kg	0.33 kg	
Standard		UL/cUL	UL/cUL	

^{*1} The maximum total memory that can be used for parameters, T/C set values, program capacity, file registers, number of comments, sampling trace, and status latch is 32 k/64 k bytes. The memory capacity is fixed. No expansion memory is available. Section 4.1.7 shows how to calculate the memory capacity.

^{*2} The I/O device after the actual input points can be used as MELSECNET(/B), MELSECNET/MINI-S3, or CC-Link.

2.1.3 A2ASCPU(S1), A2USHCPU-S1 Module Performance Specifications

The memory capacity, device performances and other specifications of A2ASCPU(S1/S30) modules are provided below.

Table 2.3 Performance specifications

Item		Performance				
		A2ASCPU	A2ASCPU -S1	A2ASCPU -S30	A2USHCPU -S1	Remarks
Control system		Stored program, repeated operation				
I/O control method		Refresh method				Instructions to enable partial direct I/O are available.
Programming language		Language dedicated to sequence control				
		Combined use of relay symbol type, logic symbolic language and MELSAP-II(SFC)				
Processing speed (sequence instruction)		0.2 μs/step		0.09 μs/step		
Constant scan (program start at specified intervals)		Can be set between 10 ms and 190 ms in 10 ms increments			Set in special register D9020.	
Memory capacity		64 kbytes (built-in RAM)				
Memory cassettes		A2SMCA-14KP/14KE		A2SMCA- 14KP/14KE A2SMCA- 30KE		
Program	Main sequence program	Max. 14k steps		Max. 30K steps		Set in parameters.
capacity	Sub sequence program	None		None		
Number of I/O device points		8192 points (X/Y0 to 1FFF)				The number of points usable in the program
Number of I/O points		512 points (X/Y0 to 1FF)		1024 points (X/Y0 to 3FF)		The number of points which can be used for accessibility to I/O modules

Table 2.3 Performance specifications (Continued)

	Performance				
Item	A2ASCPU	A2ASCPU -S1	A2ASCPU -S30	A2USHCPU -S1	Remarks
Output mode switching at STOP → RUN	Selection of re-output of operation state before STOP (default)/output after operation execution				Set in parameters.
Self-diagnostic functions	Watchdog error timer (watchdog timer 200 ms fixed) Memory error detection, CPU error detection, I/O error detection, battery error detection, etc.				Refer to Section 4.3.1 for details.
Operation mode at error occurrence	Stop or continue selectable				Set in parameters. (Refer to Section 4.4.1.)
Starting method at RUN	Initial start (Automatic restart when "RUN" switch is moved to ON position at power-on, at power restoration after power failure)				
Latch (power failure compensation) range	Defaults to L1000 to L2047 (Latch range can be set for L, B, T, C, D and W relays.)			Set range in parameters.	
Remote RUN/PAUSE contact	One RUN contact and one PAUSE contact can be set within the range from X0 to X1FF (A2AS) or X3FF (A2AS-S1/A2USH-S1)			Set range in parameters.	
Clock function	Year, month, day, hour, minute, second (Automatically recognizes leap years.) Accuracy -3.1 to +5.3 s (TYP. +1.7 s)/d at 0 °C -1.6 to +5.3 s (TYP2.1 s)/d at 55 °C -9.6 to +3.6 s (TYP2.1 s)/d at 55 °C				
Allowable momentary power failure time Depends on used power supply module			Refer to Section 6.1.		
VDC internal power onsumption 0.32 A					
Weight	0.41 kg			0.46 kg	
External dimensions	$130 \times 54.5 \times 93.6 \ (5.12 \times 2.15 \times 3.69) \ \text{mm} \ (\text{inch})$				

CAUTION

When the existing system software package and peripheral devices are used, the applicable device range is limited. Refer to Section 1.3 for details.

3. EMC DIRECTIVES AND LOW VOLTAGE DIRECTIVES

The products sold in the European countries have been required by law to comply with the EMC Directives and Low Voltage Directives of the EU Directives since 1996 and 1997, respectively.

The manufacturers must confirm by self-declaration that their products meet the requirements of these directives, and put the CE mark on the products.

(1) Authorized representative in Europe

Authorized representative in Europe is shown below.

Name: Mitsubishi Electric Europe BV

Address: Gothaer Strasse 8, 40880 Ratingen, Germany

3.1 Requirements for Compliance with EMC Directives

The EMC Directives specifies emission and immunity criteria and requires the products to meet both of them, i.e., not to emit excessive electromagnetic interference (emission): to be immune to electromagnetic interference outside (immunity).

Guidelines for complying the machinery including MELSEC-AnS series programmable controller with the EMC Directives are provided in Section 3.1.1 to 3.1.8 below.

The guidelines are created based on the requirements of the regulations and relevant standards, however, they do not guarantee that the machinery constructed according to them will not comply with the Directives.

Therefore, the manufacturer of the machinery must finally determine how to make it comply with the EMC Directives: if it is actually compliant with the EMC Directives.

3.1.1 EMC standards

(1) Regulations regarding emission

Standard	Test item	Test description	Value specified in standard
EN61131-2: 2007	CISPR16-2-3 Radiated emission *2	Radio waves from the product are measured.	• 30M-230MHz QP: 40dBµV/m (10m in measurement range) *1 • 230M-1000MHz QP: 47dBµV/m (10m in measurement range)
	CISPR16-2-1, CISPR16-1-2 Conducted emission *2	Noise from the product to the power line is measured.	150k-500kHz QP: 79dB, Mean: 66dB *1 500k-30MHz QP: 73dB, Mean: 60dB

^{*1} QP: Quasi-peak value, Mean: Average value

^{*2} The programmable controller is an open type device (device installed to another device) and must be installed in a conductive control panel. The tests for the corresponding items were performed while the programmable controller was installed inside the control panel.

(2) Regulations regarding immunity

Standard	Test item	Test description	Value specified in standard
	EN61000-4-2 Electrostatic discharge immunity *1	Immunity test in which electrostatic is applied to the cabinet of the equipment.	8kV Air discharge 4kV Contact discharge
	EN61000-4-3 Radiated, radio- frequency, electromagnetic field immunity *1	Immunity test in which electric fields are irradiated to the product.	80% AM modulation@1kHz • 80M-1000MHz: 10V/m • 1.4G-2.0GHz: 3V/m • 2.0G-2.7GHz: 1V/m
	EN61000-4-4 Electrical fast transient/burst immunity *1	Immunity test in which burst noise is applied to the power line and signal line.	AC/DC main power, I/O power, AC I/O (unshielded): 2kV DC I/O, analog, communication: 1kV
EN61131-2: 2007	EN61000-4-5 Surge immunity *1	Immunity test in which lightning surge is applied to the power line and signal line.	AC power line, AC I/O power, AC I/O (unshielded) 2kV CM, 1kV DM DC power line, DC I/O power 0.5kV CM, DM DC I/O, AC I/O (shielded), analog*2, communication: 1kV CM CM
	EN61000-4-6 Immunity to conducted disturbances, induced by radio- frequency fields *1	Immunity test in which high frequency noise is applied to the power line and signal line.	0.15M-80MHz, 80% AM modulation@1kHz, 10Vrms
	EN61000-4-8 Power-frequency magnetic field immunity *1	Immunity test in which the product is installed in inductive magnetic field.	50Hz/60Hz, 30A/m
	EN61000-4-11 Voltage dips and interruption immunity *1	Immunity test in which power supply voltage is momentarily interrupted.	Apply at 0%, 0.5 cycles and zero-cross point 0%, 250/300 cycles (50/60Hz) 40%, 10/12 cycles (50/60Hz) 70%, 25/30 cycles (50/60Hz)

^{*1} Programmable controllers are open-type devices (devices designed to be housed inside other equipment) and must be installed inside a conductive control panel. The corresponding tests were conducted with the programmable controller installed inside a control panel.

^{*2} The accuracy of an analog-digital converter module may temporary vary within \pm 10%.

3.1.2 Installation instructions for EMC Directive

The programmable controller is open equipment and must be installed within a control cabinet for use.* This not only ensures safety but also ensues effective shielding of programmable controller-generated electromagnetic noise.

- Also, each network remote station needs to be installed inside the control panel.
 - However, the waterproof type remote station can be installed outside the control panel.

(1) Control cabinet

- (a) Use a conductive control cabinet.
- (b) When attaching the control cabinet's top plate or base plate, mask painting and weld so that good surface contact can be made between the cabinet and plate.
- (c) To ensure good electrical contact with the control cabinet, mask the paint on the installation bolts of the inner plate in the control cabinet so that contact between surfaces can be ensured over the widest possible area.
- (d) Earth the control cabinet with a thick wire so that a low impedance connection to ground can be ensured even at high frequencies.
- (e) Holes made in the control cabinet must be 10 cm (3.94 in.) diameter or less. If the holes are 10 cm (3.94 in.) or larger, radio frequency noise may be emitted.
- (f) Lock the control panel so that only those who are trained and have acquiredenough knowledge of electric facilities can open the control panel.

- (2) Connection of power and earth wires Earthing and power supply wires for the programmable controller system must be connected as described below.
 - (a) Provide an earthing point near the power supply module. Earth the power supply's LG and FG terminals (LG: Line Ground, FG: Frame Ground) with the thickest and shortest wire possible. (The wire length must be 30 cm (11.18 in.) or shorter.) The LG and FG terminals function is to pass the noise generated in the programmable controller system to the ground, so an impedance that is as low as possible must be ensured. In addition, make sure to wire the ground cable short as the wires are used to relieve the noise, the wire itself carries large noise content and thus short wiring means that the wire is prevented from acting as an antenna.
 - (b) The earth wire led from the earthing point must be twisted with the power supply wires. By twisting with the earthing wire, noise flowing from the power supply wires can be relieved to the earthing. However, if a filter is installed on the power supply wires, the wires and the earthing wire may not need to be twisted.

3.1.3 Cables

The cables extracted from the control panel contain a high frequency noise component. On the outside of the control panel, therefore, they serve as antennas to emit noise. To prevent noise emission, use shielded cables for the cables which are connected to the I/O modules and intelligent function modules and may be extracted to the outside of the control panel.

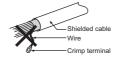
The use of a shielded cable also increases noise resistance. The signal lines (including common line) of the programmable controller, which are connected to I/O modules, intelligent function modules and/or extension cables, have noise durability in the condition of grounding their shields by using the shielded cables. If a shielded cable is not used or not grounded correctly, the noise resistance will not meet the specified requirements.

(1) Earthing of shielded of cables

- (a) Earth the shield of the shielded cable as near the unit as possible taking care so that the earthed cables are not induced electromagnetically by the cable to be earthed.
- (b) Take appropriate measures so that the shield section of the shielded cable from which the outer cover was partly removed for exposure is earthed to the control panel on an increased contact surface. A clamp may also be used as shown in the figure below. In this case, however, apply a cover to the painted inner wall surface of the control panel which comes in contact with the clamp.

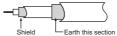


Note) The method of earthing by soldering a wire onto the shield section of the shielded cable as shown below is not recommended. The high frequency impedance will increase and the shield will be ineffective.



(2) MELSECNET (II) and MELSECNET/10 modules

(a) Use a double-shielded coaxial cable for the MELSECNET module which uses coaxial cables such as A1SJ71AR21, A1SJ71LR21 and A1SJ71BR11. Noise in the range of 30 MHz or higher in radiation noise can be suppressed by the use of double-shielded coaxial cables (manufactured by MITSUBISHI CABLE INDUSTRIES, LTD: 5C-2V-CCY). Earth the outer shield to the ground.



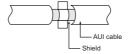
For the shield grounding, refer to item (1).

(b) Make sure to attach a ferrite core to the double-shielded coaxial cable connected to the MELSECNET module. In addition, position the ferrite core on each cable near the outlet of the control panel. The ferrite core manufactured by TDK Corporation, ZCAT3035-1330, is recommended.

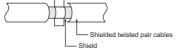
(3) Ethernet module

Precautions to be followed when AUI cables and coaxial cables are used are described below.

(a) Always earth the AUI cables*1 connected to the 10BASE5 connectors. Because the AUI cable is of the shielded type, strip part of the outer cover and earth the exposed shield section to the ground on the widest contact surface as shown helow

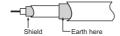


(b) Use shielded twisted pair cables as the twisted pair cables*1 connected to the 10BASE-T connectors. For the shielded twisted pair cables, strip part of the outer cover and earth the exposed shield section to the ground on the widest contact surface as shown below.



Refer to (1) for the earthing of the shield.

- *1 Make sure to install a ferrite core for the cable. The ferrite core manufactured by TDK Corporation, ZCAT2032-0930, is recommended.
- (c) Always use double-shielded coaxial cables as the coaxial cables*2 connected to the 10BASE2 connectors. Earth the double-shielded coaxial cable by connecting its outer shield to the ground.



Refer to (1) for the earthing of the shield.

*2 Make sure to install a ferrite core for the cable. The ferrite core manufactured by TDK Corporation, ZCAT3035-1330, is recommended.

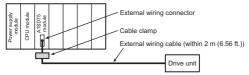
Ethernet is the registered trademark of XEROX, Co.,LTD

(4) I/O and other communication cables For the I/O signal lines (including common line) and other communication cables (RS-232, RS-422, etc), if extracted to the outside of the control panel, also ensure to earth the shield section of these lines and cables in the same manner as in item (1) above.

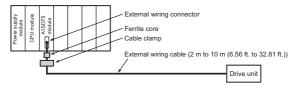
(5) Positioning Modules

Precautions to be followed when the machinery conforming to the EMC Directive is configured using the A1SD75P□-S3 are described below.

- (a) When wiring with a 2 m (6.56 ft.) or less cable
 - Ground the shield section of the external wiring cable with the cable clamp.
 (Ground the shield at the closest location to the A1SD75 external wiring connector.)
 - Wire the external wiring cable to the drive unit and external device with the shortest practicable length of cable.
 - · Install the drive unit in the same panel.



- (b) When wiring with cable that exceeds 2 m (6.56 ft.), but is 10 m (32.81 ft.) or less
 - Ground the shield section of the external wiring cable with the cable clamp.
 (Ground the shield at the closest location to the A1SD75 external wiring connector.)
 - · Install a ferrite core.
 - Wire the external wiring cable to the drive unit and external device with the shortest practicable length of cable.

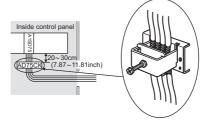


- (c) Ferrite core and cable clamp types and required quantities
 - Cable clamp
 Type: AD75CK (manufactured by Mitsubishi Electric Corporation)
 - · Ferrite core

Type: ZCAT3035-1330 (manufactured by TDK Corporation)
Contact: TDK Corporation

· Required quantity

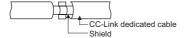
Cable length	Prepared part	Required Qty					
Cable leligili	Frepareu part	1 axis	2 axes	3 axes			
Within 2 m (6.56 ft.)	AD75CK	1	1	1			
2 m (6.56 ft.) to 10m	AD75CK	1	1	1			
(32.81 ft.)	ZCAT3035- 1330	1	2	3			



(6) CC-Link Module

(a) Be sure to ground the cable shield that is connected to the CC-Link module close to the exit of control panel or to any of the CC-Link stations within 30 cm (11.81 in.) from the module or stations.

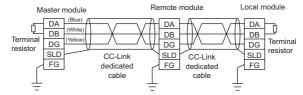
The CC-Link dedicated cable is a shielded cable. As shown in the illustration below, remove a portion of the outer covering and ground as large a surface area of the exposed shield part as possible.



(b) Always use the specified CC-Link dedicated cable.

(c) The CC-Link module, the CC-Link stations and the FG line inside the control panel should be connected at the FG terminal as shown in the diagram below.

[Simplified diagram]



- (d) Each power line connecting to the external power supply terminal or module power supply terminal must be 30m (98.43 ft) or less.
- (e) Install a noise filter to the external power supply. Use a noise filter with an attenuation characteristic equivalent to that of the MA1206 (TDK-Lambda Corporation). Note that a noise filter is not required when the module is used in Zone A defined in FN61131-2
- (f) Keep the length of signal cables connected to the analog input terminals of the following modules to 30m or less. Wire cables connected to the external power supply and module power supply terminal in the control panel where the module is installed.
 - AJ65BT-64RD3
 - AJ65BT-64RD4
 - AJ65BT-68TD
- (g) For the cable connected to the power supply terminal of the AJ65SBT-RPS, AJ65SBT-RPG or AJ65BT-68TD, attach a ferrite core with an attenuation characteristic equivalent to that of the ZCAT3035-1330 from TDK Corporation. Twist the cable around the ferrite core by one as shown below.



- (7) CC-Link/LT module To supply the CL2DA2-B and CL2AD4-B with 24VDC power using the CL1PAD1, keep the length of the power cable from the CL1PAD1 to the 24VDC power supply to 30m or less.
- (8) Measures against static electricity When using an insulation displacement connector without connector cover, a connected cable for the connector is thin in applicable wire size and coating. Therefore, note that the module may cause an electric discharge failure.

As measures against the failure, using pressure-displacement type connector whose applicable wire size is thick or soldering type connector is recommended

3.1.4 Power supply module

The precautions required for each power supply module are described below. Always observe the items noted as precautions.

Model	Precautions
A1S61PN, A1S62PN	Make sure to short the LG and FG terminals with a cable of 6 to 7cm and ground the cable.
A1S63P *1	Use the 24VDC panel power equipment conforming to the EU Directive.
A1SJHCPU(S8)	Make sure to short and ground the LG and FG terminals.*2

- *1 Filter attachment to the power cable is not required for the A1S63P product with the version (F) and later. However, use the 24VDC panel power equipment that conforms to the CE.
- *2 Make sure to attach two ferrite cores to the power line. Attach them as close to the power supply module as possible. Use a ferrite core whose damping characteristic is equivalent to that of the RFC-H13 produced by KITAGAWA INDUSTREIS CO., LTD.

3.1.5 Ferrite core

Use of ferrite cores is effective in reducing the conduction noise in the band of about 10 MHz and radiated noise in 30 to 100 MHz band. It is recommended to attach ferrite cores when the shield of the shielded cable coming out of the control panel does not work effectively, or when emission of the conduction noise from the power line has to be suppressed.

We tested using ferrite cores from TDK Corporation, ZCAT3035-1330 and ZCAT2032-0930, and RFC-H13 from KITAGAWA INDUSTREIS CO., LTD.

Make sure to attach a ferrite core to a cable at the position closest to the outlet of control panel as possible. If attached at an improper position, the ferrite core will not work effectively.

Type : ZCAT3035-1330. ZCAT2032-0930

Contact: TDK Corporation

· Ferrite core

Type : RFC-H13
Contact : KITAGAWA INDUSTREIS CO., LTD

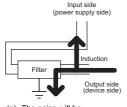
3.1.6 Noise filter (power supply line filter)

A noise filter is a component which has an effect on conducted noise. With the exception of some models, it is not required to fit the noise filter to the power supply line, but fitting it can further suppress noise. (The noise filter has the effect of reducing conducted noise of 10 M Hz or less.) Use any of the following noise filters (double π type filters) or equivalent.

Model name	FN343-3/01	FN660-6/06	ZHC2203-11			
Manufacturer	SCHAFFNER	SCHAFFNER	TDK			
Rated current	3 A	6 A	3 A			
Rated voltage	250 V					

The precautions required when installing a noise filter are described below.

(1) Do not bundle the wires on the input side and output side of the noise filter. When bundled, the output side noise will be induced into the input side wires from which the noise was filtered.



- Input side (power supply side)

 Filter

 Output side (device side)
- (a) The noise will be included when the input and output wires are bundled
- (b) Separate and lay the input and output wires.
- (2) Earth the noise filter earthing terminal to the control cabinet with the shortest wire possible (approx. 10 cm (3.94 in.)).

3.1.7 Power line for external power supply terminal

Use a CE-marked AC/DC power supply for an external power supply of the modules, and the power cable length needs to be less than 30m (98.43 ft.).*1

*1 The power cable length for the A1SJ71E71N-B5 needs to be less than 3m (9.84 ft.).

Install noise filters to external supply power terminals of the I/O module and the modules below.

Use noise filters whose damping characteristic is equivalent to that of the MA1206 produced by TDK Lambda Corporation.

- · Analog-digital converter module
- · Digital-analog converter module
- · Analog I/O module
- · Temperature input module
- · Temperature control module
- · Pulse input module
- · High-speed counter module
- · Positioning module

3.1.8 Installation environment of the CC-Link/LT module and the AS-i module

(1) CC-Link/LT module

Use the module under the environment of Zone A*1. For the categories of the following products, refer to the manual came with each product.

- CL1Y4-R1B1
- CL1Y4-R1B2
- CL1XY4-DR1B2
- CL1XY8-DR1B2
- CL1PSU-2A

(2) AS-i module

Use the module under the environment of Zone A*1

- *1 Zone defines categories according to industrial environment, specified in the EMC and Low Voltage Directives, EN61131-2.
 - Zone C: Factory mains (isolated from public mains by dedicated transformer)
 - Zone B: Dedicated power distribution, secondary surge protection (rated voltage: 300V or less)
 - Zone A: Local power distribution, protected from dedicated power distribution by AC/DC converter and insulation transformer (rated voltage: 120V or less)

3.2 Requirements for Compliance with Low Voltage Directives

The Low Voltage Directives apply to the electrical equipment operating from 50 to 1000VAC or 75 to 1500VDC; the manufacturer must ensure the adequate safety of the equipment.

Guidelines for installation and wiring of MELSEC-AnS series programmable controller are provided in Section 3.2.1 to 3.2.7 for the purpose of compliance with the EMC Directives.

The guidelines are created based on the requirements of the regulations and relevant standards, however, they do not guarantee that the machinery constructed according to them will comply with the Directives

Therefore, the manufacturer of the machinery must finally determine how to make it comply with the EMC Directives: if it is actually compliant with the EMC Directives.

3.2.1 Standard applied for MELSEC-AnS series programmable controller

The standard applied for MELSEC-AnS series programmable controller series is EN61010-1 safety of devices used in measurement rooms, control rooms, or laboratories.

For the modules which operate with the rated voltage of 50 VAC/75 VDC or above, we have developed new models that conform to the above standard.

For the modules which operate with the rated voltage under 50 VAC/75 VDC, the conventional models can be used, because they are out of the low voltage directive application range.

3.2.2 Precautions when using the MELSEC-AnS series programmable controller

Module selection

(1) Power module

For a power module with rated input voltage of 100/200 VAC, select a model in which the internal part between the first order and second order is intensively insulated, because it generates hazardous voltage (voltage of 42.4 V or more at the peak) area. For a power module with 24 VDC rated input, a conventional model can be used.

(2) I/O module

For I/O module with rated input voltage of 100/200 VAC, select a model in which the internal area between the first order and second order is intensively insulated, because it has hazardous voltage area.

For I/O module with 24 VDC rated input, a conventional model can be used.

- (3) CPU module, memory cassette, base unit Conventional models can be used for these modules, because they only have a 5 VDC circuit inside.
- (4) Special function module Conventional models can be used for the special modules including analog module, network module, and positioning module, because the rated voltage is 24 VDC or smaller.
- (5) Display device Use the CE-marked product.

3.2.3 Power supply

The insulation specification of the power module was designed assuming installation category II. Be sure to use the installation category II power supply to the programmable controller.

The installation category indicates the durability level against surge voltage generated by a thunderbolt. Category I has the lowest durability; category IV has the highest durability.

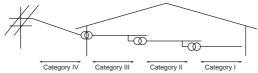


Figure 1.: Installation Category

Category II indicates a power supply whose voltage has been reduced by two or more levels of isolating transformers from the public power distribution.

3.2.4 Control panel

Because the programmable controller is an open device (a device designed to be stored within another module), be sure to use it after storing in the control panel.

- (1) Electrical shock prevention In order to prevent persons who are not familiar with the electric facility such as the operators from electric shocks, the control panel must have the following functions:
 - (a) The control panel must be equipped with a lock so that only the personnel who has studied about the electric facility and have enough knowledge can open it.
 - (b) The control panel must have a structure which automatically stops the power supply when the box is opened.
 - (c) For electric shock protection, use IP20 or greater control panel.

(2) Dustproof and waterproof features

The control panel also has the dustproof and waterproof functions. Insufficient dustproof and waterproof features lower the insulation withstand voltage, resulting in insulation destruction. The insulation in our programmable controller is designed to cope with the pollution level 2, so use in an environment with pollution level 2 or below.

Pollution level 1: An environment where the air is dry and conductive dust does not exist.

Pollution level 2 : An environment where conductive dust does not usually exist, but occasional temporary conductivity occurs due to the accumulated dust.

Generally, this is the level for inside the control panel equivalent to IPS4 in a control room or on the floor of a

typical factory.

Pollution level 3: An environment where conductive dust exits and conductivity may be generated due to the accumulated dust

An environment for a typical factory floor.

Pollution level 4: Continuous conductivity may occur due to rain, snow, etc.

An outdoor environment.

As shown above, the programmable controller can realize the pollution level 2 when stored in a control panel equivalent to IP54.

3.2.5 Module installation

(1) Installing modules contiguously

In QAnS series programmable controllers, the left side of each I/O module is left open. When installing an I/O module to the base, do not make any open slots between any two modules. If there is an open slot on the left side of a module with 100/200 VAC rating, the printed board which contains the hazardous voltage circuit becomes bare. When it is unavoidable to make an open slot, be sure to install the blank module (A1SG60).

3.2.6 Grounding

There are two kinds of grounding terminals as shown below. Either grounding terminal must be used grounded.

Be sure to ground the protective grounding for the safety reasons.

Protective grounding : Maintains the safety of the programmable controller and improves the noise resistance.

Functional grounding 👍 : Impr oves the noise resistance.

3.2.7 External wiring

(1) Module power supply and external power supply For the remote module which requires 24VDC as module power supply, the 5/12/24/48VDC I/O module, and the intelligent function module (special function module) which requires the external power supply, use the 5/12/24/48VDC circuit which is doubly insulated from the hazardous voltage circuit or use the power supply whose insulation is reinforced.

(2) External devices

When a device with a hazardous voltage circuit is externally connected to the programmable controller, use a model whose circuit section of the interface to the programmable controller is intensively insulated from the hazardous voltage circuit.

(3) Intensive insulation

Intensive insulation refers to the insulation with the dielectric withstand voltage shown in Table 1.

Table 1 :Intensive Insulation Withstand Voltage (Installation Category II, source : IEC664)

Rated voltage of hazardous voltage area	Surge withstand voltage (1.2/50 µs)
150 VAC or below	2500 V
300 VAC or below	4000 V

4. LOADING AND INSTALLATION

4.1 Installing the Module

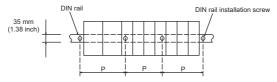
4.1.1 Notes on handling the module

This section explains some notes on handling the CPU module, I/O module, special function module, power supply module, and base unit.

- Do not drop or allow any impact to the modules case, memory card, terminal block cover, or pin connector.
- (2) Do not remove the module printed wiring board from the case. Otherwise, a malfunction may occur.
- (3) Use caution to prevent foreign matter, such as wire chips, falling into the module during wiring. If foreign matter enters the module, remove it.
- (4) Tighten screws (such as a module fixingscrew) within the tightening torque range specified in the table below.

Screw portion	Tightening torque range
Module fixing screw (M4 screw)	78 to 118 N·cm
I/O module (M3.5 screw)	59 to 88 N·cm
Power supply module terminal screws (M3.5 screw)	59 to 78 N·cm

- (5) Observe the following points when you are installing the DIN rail:
 - (a) Applicable DIN rail type (JIS C 2812) TH35-7.5Fe TH35-7.5Al TH35-15Fe
 - (b) DIN rail installation screw interval When you are using the TH35-7.5Fe or TH35-7.5Al DIN rail, tighten the DIN rail installation screw with a pitch of 200 mm (7.87 inch) or less to maintain the strength.



(6) When installing the base unit to DIN rail in an environment with large vibration, use a vibration-proofing bracket (A1S-PLT-D). Mounting the vibration-proofing bracket (A1S-PLT-D) enhances the resistance to vibration. Depending on the environment to set up the base unit, it is also recommended to fix the base unit to the control panel directly.

4.1.2 Installation environment

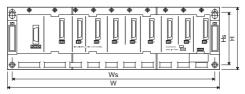
Avoid the following environment when you install the CPU system:

- A location in which the ambient temperature falls outside the range of 0 to 55 degrees Celsius.
- (2) A location in which the ambient humidity falls outside the range of 10 to 90%RH.
- (3) A location in which condensation may occur due to drastic changes in temperature.
- (4) Tighten screws (such as a module fixingscrew) within the tightening torque range specified in the tablebelow.
- (5) A location in which the system is easily exposed to conductive powder, such as dust and iron filings, oil mist, salt, or organic solvent
- (6) A location exposed to direct sunlight.
- (7) A location in which strong electrical or magnetic fields are generated.
- (8) A location in which the module is exposed to direct vibration or impact.

4.1.3 Notes on installing the base unit

Take ease of operation, ease of maintenance, and environmental durability into consideration when you are installing the programmable controller on the panel.

(1) Mounting dimension Mounting dimensions of each base unit are as follows:



	A1S32B	A1S33B	A1S35B	A1S38B	A1S52B (S1)	A1S55B (S1)	A1S58B (S1)	A1S65B (S1)	A1S68B (S1)
W	220 (8.66)	255 (10.04)	325 (12.80)	430 (16.93)	155 (6.10)	260 (10.24)	365 (14.37)	315 (12.40)	420 (16.54)
Ws	200 (7.87)	235 (9.25)	305 (12.01)	410 (16.14)	135 (5.31)	240 (9.45)	345 (13.58)	295 (11.61)	400 (15.75)
Н	130 (5.12)								
Hs	110 (4.33)								

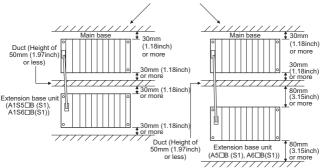
Dimensions: mm (inch)

(2) Module installation position

To maintain good ventilation and make it easy to replace the module, keep the following distances between the top and bottom of the module and the structure or other components.

- A5□B, A6□B.....80 mm (3.15 inch) or over

This shows the position of the panel's ceiling, wiring duct, or components.



(3) Module installation direction

(a) Use the programmable controller in the following position for better ventilation and heat dissipation:



(b) Do not use the programmable controller in the following positions:



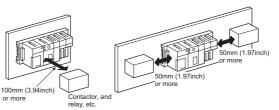


Vertical position

Horizontal position

- (4) Install the base unit on a level surface. If the surface is not level, force may be applied to the printed wiring board, causing a malfunction.
- (5) Install the unit far from any source of vibration, such as a large magnetic contactor and a no-fuse breaker on the same panel, or install it on a separate panel.
- (6) Keep the following distance between the programmable controller and other devices (such as a contactor and a relay) in order to avoid the influence of radiated noise and heat:
 - a device installed in front of the programmable controller
 100mm (3.94 inch) or more
 - a device installed on the right or left of the programmable controller

......50mm (1.97 inch) or more



4.2 Fail-Safe Circuit Concept

When the programmable controller is powered ON and then OFF, improper outputs may be generated temporarily depending on the delay time and start-up time differences between the programmable controller power supply and the external power supply for the control target (especially, DC).

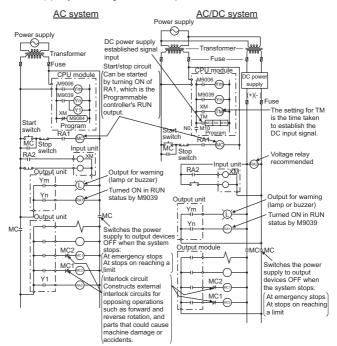
For example, if the external power supply for the control target is powered ON and then the programmable controller is powered ON, the DC output module may generate incorrect outputs temporarily upon the programmable controller power-ON. Therefore, it is required to build the circuit that energizes the programmable controller by priority.

The external power failure or programmable controller failure may lead to the system error.

In order to eliminate the possibility of the system error and ensure failsafe operation, build the following circuit outside the programmable controller: emergency circuit, protection circuit and interlock circuit, as they could cause machine damages and accidents due to the abovementioned failures.

An example of system design, which is based on fail-safe concept, is provided on the next page.

(1) System design circuit example



The procedures used to switch on the power supply are indicated below.

AC system

- [1] Switch the power supply ON.
- [2] Set the CPU module to RUN.
- [3] Switch the start switch ON.
- [4] The output devices are driven in accordance with program when the magnetic contactor (MC) turns ON.
- AC/DC system [1] Switch the power supply ON.
- [2] Set the CPU module to RUN.
- [3] Switch RA2 ON when the DC power supply starts.
- [4] Switch the timer (TM) ON when the DC power supply reaches working voltage. (The set value for TM must be the time it takes for 100% establishment of the DC power after RA2 is switched ON. Make this set value 0.5 seconds.)
- switched ON. Make this set value 0.5 seconds. [5] Switch the start switch ON.
- [6] The output devices are driven in accordance with the program when the magnetic contactor (MC) comes ON. (If a voltage relay is used at RA2, no timer (TM) is necessary in the program.)

(2) Fail-safe measures to cover the possibility of programmable controller failure

Problems with a CPU module and memory can be detected by the self diagnostics function. However, problems with I/O control area may not be detected by the CPU module.

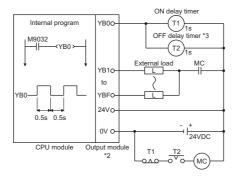
In such cases, all I/O points turn ON or OFF depending on the problem, and normal operation and safety cannot be maintained. Though Mitsubishi programmable controllers are manufactured under strict quality control, they may fail or malfunction due to unspecified reasons. To prevent the whole system failure, machine breakdown, and accidents, build a fail-safe circuit outside the programmable controller.

Examples of a system and its fail-safe circuitry are described below:

<System example>

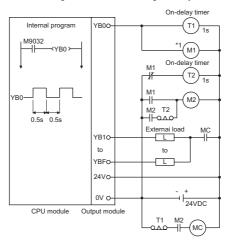
lп	Power supply module	CPU module	Input 16 points	Input 16 points	Input 16 points	16	16	16	16	Output 16 points	Ш		Power supply module	16	16	Output 16 points	points	Vacant
												J					1	

*1 The output module for fail-safe purpose should be mounted on the last slot of the system. (YB0 to YBF in the above system.)



- *2 Since YB0 turns ON and OFF alternatively at 0.5 second intervals, use a contactless output module (a transistor is used in the above example).
- *3 If an offdelay timer (especially miniature timer) is not available, construct the failsafe circuit using an ondelay timer shown on the next page.

When constructing a failsafe circuit using ondelay timers only



*1 Use a solid state relay for the M1 relay.

4.3 Wiring

4.3.1 Power supply module specifications

(1) Table 4.1 shows the specifications of the power supply modules.

Table 4.1 Power supply module specifications

	Itama	Specifications						
	Item	A1S61PN	A1S62PN	A1S63P				
Base unit posi	ition	Power supply module slot						
Rated input v	oltage			24 V DC (+ 30 %/- 35 %)				
			64 V AC)	(15.6 to 31.2 V DC)				
Rated input fr	requency	50/60 H	Iz ± 5 %	-				
Input voltage	distortion factor		er to Section 4.4)	-				
Max. input ap	parent power	105	VA	41 W				
Inrush curren		20 A 8 ms	or lower *4	81 A 1 ms or lower				
Rated output		5 A	3 A	5 A				
current	24 V DC \pm 10 %	-	0.6 A	-				
	5 V DC	5.5 A or higher	3.3 A or higher	5.5 A or higher				
protection *1		-	0.66 A or higher					
	5 V DC		5.5 to 6.5	V				
protection *2	24 V DC	-						
Efficiency		65 % or higher						
Allowable mo failure time *3	mentary power	20 ms	or lower	1 ms or lower				
Dielectric	Between primary and 5 V DC		t/LG and output/ G	500 V AC				
withstand voltage	Between primary and 24 V DC		rms/3 cycle (6562 ft.))	-				
Insulation res	istor	AC across input/LG and output/FG 10 M Ω or higher, measures with a 500 V DC insulation resistance tester						
Noise durabili	ity	simulator co	1µs, Noise 5 to 60 Hz (noise	Noise voltage 500 Vp-p, Noise width 1µs, Noise frequency 25 to 60 Hz (noise simulator condition)				
Power indicat	tion	Power LED indication (light at the time of output of 5 V DC)						
Terminal scre	w size	M3.5 × 7						
Applicable wir	re size	0.75 to 2 mm ² (AWG 18 to 14)						
Applicable so	Iderless terminal	RAV 1.25 to 3.5, RAV 2 to 3.5						
Applicable tig	htening torque	59 to 88 N-cm						
External dime	ension	130 × 55 :	× 93.6 (5.12 × 2.1	7 × 3.69) mm (inch)				
Weight		0.60 kg 0.50 kg						

POINT

*1 Overcurrent protection

The overcurrent proctection device shuts off the 5VDC and/or 24VDC circuit(s) and stops the system if the current exceeding the specified value flows in the circuit(s). As this results in voltage drop, the power supply module LED turns OFF or is dimly lit.

After that, eliminate the causes of overcurrent, e.g., insufficient current capacity and short circuit, and then start the system.

When the current has reached the normal value, the initial start up of the system will be performed.

*2 Overvoltage protection

The overvoltage protection shuts off the 5VDC circuit and stops the system if the overvoltage of 5.5 to 6.5V is applied to the circuit.

This results in the power supply module LED turning OFF.

When restarting the system, power OFF and ON the input power supply, and the initial start up of the system will be performed.

If the system is not booted and the LED remains off, this means that the power supply module has to be replaced.

*3 Allowable momentary power failure period

The programmable controller CPU allowable momentary power failure period varies with the power supply module used.

In case of the A1S63P power supply module, the allowable momentary power failure period is defined as the time from when the primary side of the stabilized power supply for supplying 24VDC to the A1S63P is turned OFF until when the voltage (secondary side) has dropped from 24VDC to the specified value (15.6VDC) or less.

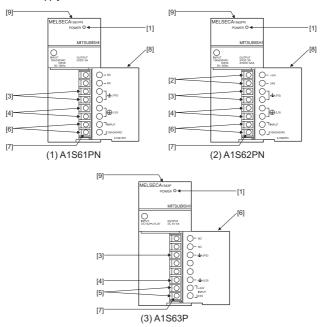
*4 Inrush current

If the power supply module is re-powered ON right after powered OFF (within 5seconds), the inrush current exceeding the specified value (2ms or less) may be generated. Therefore, make sure to re-power ON the module 5seconds after power off.

When selecting a fuse or breaker for external circuit, consider the above point as well as meltdown and detection characteristics.

4.3.2 Parts names

The following gives the names and description of the parts of the power supply modules :



No.	Name	Description
[1]	POWER LED	The indicator LED for the 5 V DC power.
[2]	24 V and 24 G terminals	Used to supply 24 V DC to inside the output module (using external wiring).
[3]	FG terminal	The grounding terminal connected to the shield pattern of the printed circuit board.
[4]	LG terminal	Grounding for the power supply filter. The potential of A1S61PN or A1S62PN terminal is 1/2 of the input voltage.
[5]	Power supply input terminals	Used to connect a 24 V DC power supply.

No.	Name	Description
[6]	Power supply input terminals	Used to connect 100 V AC to 200 V AC power supply.
[7]	Terminal screw	M3.5 × 7
[8]	Terminal cover	The protective cover of the terminal block.
[9]	Module fixing screw	Used to fix the module to the base unit. (M4 screw, tightening torque: 78 to 118 N·cm)

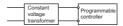
POINT

- (1) Do not cable to the unused terminals such as FG and LG on the terminal block (terminals whose name is not printed on the terminal cover).
- (2) Be sure to ground the terminal LG to the protective ground conductor with class D (class-3) grounding or above.

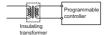
4.3.3 Wiring instructions

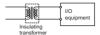
Instructions for wiring the power supply cable and I/O cable.

- (1) Wiring of power supply
 - (a) When voltage fluctuations are larger than the specified value, connect a constant-voltage transformer.



(b) Use a power supply which generates minimal noise between wires and between the PLC and ground. If excessive noise is generated, connect an insulating transformer.





(c) When a power transformer or insulating transformer is employed to reduce the voltage from 200 VAC to 100 VAC, use one with a capacity greater than those indicated in the following table.

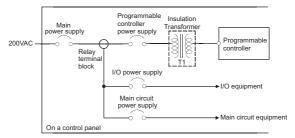
Power Supply Module	Transformer Capacity
A1S61PN	110VA × n
A1S62PN	110VA × n

 Stands for the number of power supply modules.

(d) Separate the programmable controller's power supply line from the lines for I/O devices and power devices as shown below.

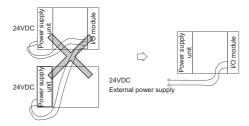
When there is much noise, connect an insulation transformer.

(e) Taking rated current or inrush current into consideration when wiring the power supply, be sure to connect a breaker or an external fuse that have proper blown and detection. When using a single programmable controller, a 10A breaker or an external fuse are recommended for wiring protection.



(f) Note on using the 24 VDC output of the A1S62PN power supply module.

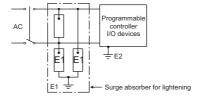
If the 24 VDC output capacity is insufficient for one power supply module, supply 24 VDC from the external 24 VDC power supply as shown below:



(g) 100 VAC, 200 VAC and 24 VDC wires should be twisted as dense as possible. Connect the modules with the shortest distance.

Also, to reduce the voltage drop to the minimum, use the thickest wires possible (maximum 2mm²).

- (h) Do not bind 100VAC and 24VDC wires together with main circuit (high tension and large current) wires or I/O signal lines (including common line) nor place them near each other. Provide 100mm (3.94 inch) clearance between the wires if possible.
- As a countermeasure to power surge due to lightening, connect a surge absorber for lightening as shown below.



POINT

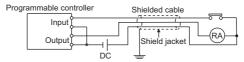
- Separate the ground of the surge absorber for lightening (E1) from that of the programmable controller (E2).
- (2) Select a surge absorber for lightening whose power supply voltage does no exceed the maximum allowable circuit voltage even at the time of maximum power supply voltage elevation

(2) Wiring to I/O device

- (a) The solderless terminal with insulation sleeve is inapplicable to a terminal block.
 - It is advisable to cover the wire connection part of a terminal with a mark tube or insulation tube.
- (b) Install wiring to a terminal block using the cable of core diameter 0.3 to 0.75mm², and outside diameter 2.8mm or less.
- (c) Run the I/O line and output line away from each other.

(d) When the main circuit line and power line cannot be separated, use a shielding cable and ground it on the programmable controller side.

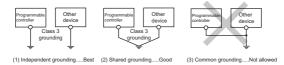
However, ground it on the opposite side in some cases.



- (e) When cables are run through pipes, securely ground the pipes.
- (f) Run the 24VDC input line away from the 100VAC and 200 VAC lines.
- (g) The cabling of 200m (656.2ft.) or longer distance may produce leakage current depending on the capacity between lines and result in an accident.
- (h) As a countermeasure against the power surge due to lightning, separate the AC wiring and DC wiring and connect a surge absorber for lightning as shown in (i) of item (1). Failure to do so increases the risk of I/O device failure due to lightning.

(3) Grounding

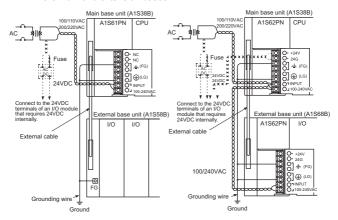
- (a) Carry out the independent grounding if possible. (Grounding resistance 100Ω or less.)
- (b) If the independent grounding is impossible, carry out the shared grounding (2) as shown below.



- (c) Use the cable of 2mm² or more for grounding. Set the grounding point closer to the programmable controller to make the grounding cable short as possible.
- (d) If a malfunction occurs due to earthling, separate either LG or FG of the base module, the device combination, or all the connection from the earthling.

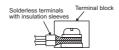
4.3.4 Wiring to module terminals

This section explains the wiring of power lines and grounding lines to the main and extension bases.



POINT

(1) Use the thickest possible (max. 2 mm² (14 AWG)) wires for the 100/200 VAC and 24 VDC power cables. Be sure to twist these wires starting at the connection terminals row wiring a terminal block, be sure to use a solderless terminal. To prevent short-circuit due to loosening screws, use the solderless terminals with insulation sleeves of 0.8 mm (0.03 inch) or less thick. The number of the solderless terminals to be connected for one terminal block are limited to 2.



- (2) Be sure to ground the LG and FG terminals. Failure to do so may cause the programmable controller to be susceptible to noise. Note that LG terminals include the potential as half as that of input voltage; you might get an electric shock when you touch them.
- (3) A1S61PN and A1S62PN do not need to be switched as the are 100 to 240VAC widerange.

4.4 Precautions when Connecting the Uninterruptible Power Supply (UPS)

Connect the programmable controller system to the uninterruptible power supply (UPS), while paying attention to the followings.

When connecting an uninterruptible power supply (UPS) to the programmable controller system, use an online UPS or line-interactive UPS with a voltage distortion rate of 5% or less.

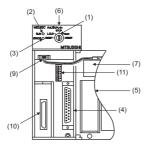
When connecting a standby UPS, use a Mitsubishi FREQUPS FW-F series UPS (hereinafter FW-F series UPS)*1. (Example: FW-F10-0.3K/0.5K)

Do not use any standby UPS other than the FW-F series UPS.

*1 The FW-F series UPS whose serial number starts with the letter "P" or later, or ends with the letters "HE" is applicable

4.5 Part Names

This section gives part names of the CPU.



A2ASCPU(S1/S30)/A2USHCPU-S1

(1) RUN/STOP key switch

RUN/STOP:Used to start/stop sequence program execution. L.CLR:Used to clear the data set in the latch range set in the parameters.

(2) [RUN] LED

ON:

The RUN/STOP switch is set to "RUN" and sequence program is in operation.

OFF:

100/200VAC is not supplied to the CPU.

The RUN/STOP switch is set to "STOP".

During STOP/PAUSE execution.

Flicker:

Self-diagnostic detected a sequence program error to stop the operation.

During latch clear operation.

(3) [ERROR] LED

ON:

Self-diagnostic error is detected. (when an error set to OFF is detected by the LED indication priority setting, the switch stays OFF)

OFF:

When failure of the system or target device is detected by normal or CHK instruction.

Flicker:

Annunciator (F) is turned on in the sequence program.

- (4) RS-422 connector
- (5) Cover
- (6) Module fixing screws
- (7) Battery
- (8) Dip switch



- (9) Battery connector
- (10) Memory cassette installing connector

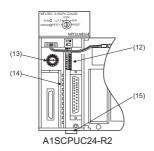
(11) Memory write protect Dip switch



Memory Write	Setting Switch		
Protect Range (kbytes)	A2SHCPU A2ASCPU	A2SHCPU-S1 A2ASCPU-S1/S30 A2USHCPU-S1	
0 to 16	1: ON	1: ON	
16 to 32	2: ON	2: ON	
48 to 64	4: ON	4: ON	
64 to 80	Unused	5: ON	
80 to 96	Unused	6: ON	
96 to 112	Unused	7: ON	
112 to 144	Unused	8: ON	
144 to 256	Unused	9: ON	

Latch clear method

- Turn the RUN/STOP key switch from the "STOP" position to the "L.CLR" position several times to make the "RUN" LED start flashing.
- Turn the RUN/STOP switch from the "STOP" position to the "L.CLR" position once more.
- 3) The latch clear operation is completed when the "RUN" LED goes OFF.



(12) Transmission specification setting switch

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1	Transmission specification settings (factory setting: all OFF)						
sw	Catting Itama	Position of Setting Switch					
SVV	Setting Items	ON	OFF				
1	Write during RUN enabled/ disabled setting	Enabled	Disabled				
2			•				
3	Transmission speed setting	Refer to *1					
4							
5	Data bit setting	8 bit	7 bit				
6	Parity bit setting	Set	Not set				
7	Even/odd parity setting	Even	Odd				
8	Stop bit setting	2 bit	1 bit				
9	Sum check setting	7Set	Not set				

*1 Transmission speed setting							
Baud rate (BPS)	Baud rate (BPS) 300 600 1200 2400 4800 9600 19200						
SW05	OFF	ON	OFF	ON	OFF	ON	OFF
SW06	OFF	OFF	ON	ON	OFF	OFF	ON
SW07	OFF	OFF	OFF	OFF	ON	ON	ON

(13) Mode setting switch



2 [3 [4 [5 [6 [7 [8 [9 [

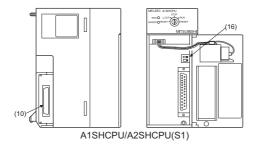
Mode settings (factory setting: 0)					
Mode Setting Switch Number	Setting				
0	Unusable				
1	Protocol 1				
2	Protocol 2				
3	Protocol 3				
4	Protocol 4				
5	No-protocol or printer function				
6 to E	Unusable				
F	Used for testing the independent module				

(14) Computer link LED

LED No.	Description					
0	RUN	Normal run Normal: ON Error: OFF				
1	SD	Transmitting Flashes during data transmission				
2	RD	Receiving Flashes during data receive				
3	CPU	Communications with programmable controller CPU Flashes during communications with programmable controller CPU				
4	NEU	Neutral Transmission sequence initial state (waiting for ENQ): ON ENQ received: OFF				
5	ACK	ACK After sending ACK: ON After sending NAK: OFF				
6	NAK	NAK After sending NAK: ON After sending ACK: OFF				
7	C/N	Result of programmable controller CPU communications Error: ON Normal: OFF				
8	P/S	Parity/Sum check error Error: ON Normal: OFF				
9	PRO	Protocol error Communications protocol error: ON Normal: OFF				
10	SIO	SIO error Normal: OFF				

(15) RS-232C connector

Refer to the following manual for computer link function.
Computer Link/Multidrop unit User's Manual
(Computer link/Printer function) SH-3511



(16) Dip switch



Switch No.	Application
2	Memory write protect setting (RAM and E ² PROM) ON: With memory write protection *1 OFF: Without memory write protection
1	I/O select switch setting ON: Direct method OFF: Refresh method

*1 When installing the memory cassette, the setting becomes invalid to RAM only.



Switch No.	Application				
5	I/O switch setting ON: Direct method OFF:Refresh method				
	Memory write protect setting (RAM and E ² PROM) ON: With memory write protection *1 OFF:Without memory write protection				
Memory write protection range					
4	48 to 64k bytes				
3	32 to 48k bytes				
2	16 to 32k bytes				
1	0 to 16k bytes				

*1 When installing the memory cassette, the setting becomes invalid to RAM only.



Switch No.	Application			
10	I/O select switch setting ON: Direct method OFF:Refresh method			
Memory write protect setting (RAM and E ² PROM) ON:With memory write protection *1 OFF:Without memory write protection				
	Memory write protection range			
9	144 to 192k bytes			
8	112 to 144k bytes			
7	96 to 112k bytes			
6	80 to 96k bytes			
5	64 to 80k bytes			
4	48 to 64k bytes			
3	32 to 48k bytes			
2	16 to 32k bytes			
1	0 to 16k bytes			

*1 When installing the memory cassette, the setting becomes invalid to RAM only.

4.6 RS232C Interface (A1SCPUC24-R2 only)

(1) RS-232C connector specifications

	Pin Number	Signal Abbreviation	Signal Name	Signal Direction A1SCPUC24-R2 →External Device
	1	CD	Receive carrier detection	-
6 • 7 • 8 • 90	2	RD(RXD)	Receive data	+
8.	3	SD(TXD)	Send data	
30	4	DTR(ER)	Data terminal ready	-
	5	SG	Signal ground	←
	6	DSR(DR)	Data set ready	-
	7	RS(RTS)	Request to send	-
	8	CS(CTS)	Clear to send	+

A 9-pin D subconnector is supplied in the same package as the A1SCPUC24-R2 body.

Product name

9-pin Dsub (male), screw mounted 17JE-23090-02-D8A, made by DDK

(2) RS-232C cable

For the RS-232C cable, use a cable that conforms to the RS-232C standard and is no longer than 15 m. (Recommended cable)

7/0. 127[]P HRV-SV......(RS-232C cable made by Oki Densen)

Specify the number of wire pairs.

For example, if the number of pairs is thirteen:

7/0 127 13P HRV-SV

(3) Connecting the RS-232C connectors

The standard method for connecting the RS-232C connectors is shown below.

For details on the connection method, refer to the Computer Link Module User's Manual (Com. link func./Print func.).

(a) Example connection to an external device in which the CD signal (pin No.8) can be switched ON and OFF.

A1SCPUC24-R2		Cable	External Device
Signal Names	Pin Number	Connections and Signal Directions	Signal Names
CD	1	х л	CD
RD(RXD)	2	\	RD(RXD)
SD(TXD)	3		SD(TXD)
DTR(ER)	4		DTR(ER)
SG	5	—	SG
DSR(DR)	6	—	DSR(DR)
RS(RTS)	7		RS(RTS)
CS(CTS)	8	\vdash	CS(CTS)

- (b) Example connection to an external device in which the CD signal (pin No.8) cannot be switched ON and OFF. In the case of a connection to a device in which the device's CD signal cannot be switched ON and OFF, set non-execution of the buffer memory address 10BH RS232C CD terminal check.
 - Example connection to an external device in which DC code control or DTR/DSR code control is executed.

A1SCPUC24-R2		Cable Connections and	External Device
Signal Names	Pin Number	Signal Directions	Signal Names
CD	1		CD
RD(RXD)	2	←	RD(RXD)
SD(TXD)	3		SD(TXD)
DTR(ER)	4		DTR(ER)
SG	5	$\qquad \qquad \longrightarrow$	SG
DSR(DR)	6	,	DSR(DR)
RS(RTS)	7		RS(RTS)
CS(CTS)	8	\leftarrow	CS(CTS)

Example connection to an external device in which DC code control is executed.

A1SCPUC24-R2		Cable	External Device
Signal Names	Pin Number	Connections and Signal Directions	Signal Names
CD	1		CD
RD(RXD)	2	←	RD(RXD)
SD(TXD)	3		SD(TXD)
DTR(ER)	4		DTR(ER)
SG	5	 ← →	SG
DSR(DR)	6	←	DSR(DR)
RS(RTS)	7		RS(RTS)
CS(CTS)	8	←	CS(CTS)

4.7 Self-Loopback Test (A1SCPUC24-R2 only)

The self-loopback test checks whether or not the isolated A1SCPUC24-R2 (not connected to any external devices) will operate correctly.

For details on the self-loopback test, refer to the Computer Link Module User's Manual (Com. link func./Printer func.).

Connect the cables

Connect cables to the RS-232C connectors.

_	
1 • 2 • 3 • 4 • 5 •	6 ● 7 ● 8 ● 9 O

Pin Number	Signal Abbreviation	Signal Name	Cable Connections
1	CD	Receive carrier detection	<u></u>
2	RD(RXD)	Receive data	←
3	SD(TXD)	Send data	
4	DTR(ER)	Data terminal ready	
5	SG	Signal ground	-
6	DSR(DR)	Data set ready	—
7	RS(RTS)	Request to send	
8	CS(CTS)	Clear to send	→

Set the mode setting switch

Set the mode setting switch to "F".

Execute the self-loopback test

 Turn the programmable controller CPU power supply ON or reset the programmable controller CPU.

Check the LED display status

Check Item	Display When Normal		Display in Error Status		
Programmable controller	C/N	OFF	C/N	ON	
CPU communications check	CPU	Flicker	(LED No.7)	ON	
DC 222C semmunications	SIO	OFF	210		
RS-232C communications check	SD	Flicker	SIO (LED No.10)	ON	
CHECK	RD	FIICKEI	(LLD NO. 10)		

Completed

Turn the power supply OFF.

5. SPECIFICATION AND CONNECTION OF I/O MODULES

5.1 Input modules

5.1.1 Input module specifications

					Operatin	g Voltage			
Model	Туре	No. of Points	Rated Input Voltage	Input Current	ON voltage	OFF voltage			
A1SX10			100 to	6mA					
A1SX10EU	AC input		120VAC	7mA	80VAC or	30VAC or			
A1SX20	· ·		200 to	9mA	higher	lower			
A1SX20EU			240VAC	11mA					
A1SX30	K30 DC/AC input 16	16	12/24VDC	4.2/	7VDC/AC or	2.7VDC/AC or			
ATOXOO		10	12/24VAC	8.6mA	higher	lower			
A1SX40					12/24VDC	3/7mA	8VDC or higher	4VDC or lower	
A1SX40-S1			24VDC	7mA	14VDC or higher	6.5VDC or lower			
A1SX41			12/24VDC	3/7mA	8VDC or higher	4VDC or lower			
A1SX41-S1	DC input	32			17VDC or	3.5VDC or			
	(sink type)		24VDC	7mA	higher 14VDC or	lower 6.5VDC or			
A1SX41-S2					higher	lower			
A1SX42			12/24VDC	2/5mA	8VDC or higher	4VDC or lower			
A1SX42-S1		64	24VDC	5mA	18.5VDC or higher	3VDC or lower			
A1SX42-S2			24VDC	JIIIA	17.5VDC or higher	7VDC or lower			

 Maximum	Max. Response Time				Internal		
Simultaneous Input Points (Percentage Simultaneously ON)	OFF to ON	ON to OFF	Field Wiring	Points/ Common	Current Con- sumption (5VDC)	NO. of Occupied Points	
100%(110VAC) 60%(132VAC) 100%(110VAC)	20ms or lower	35ms or lower					
60%(220VAC)	30ms or lower	55ms or lower		16 0.05A			
75%(26.4VDC)	20ms or lower	20ms or lower	Terminal		0.05A	0.054	16
7370(20.4400)	25ms or lower	20ms or lower	remina	10		10	
100%(26.4VDC)	10ms or lower 0.1ms or lower	10ms or lower 0.2ms or lower					
	10ms or lower	10ms or lower			0.08A		
60%(26.4VDC)	0.3ms or lower	0.3ms or lower	40-pin connector		0.12A	32	
	10ms or	10ms or		32	A80.0		
	lower	lower	40-pin	52	0.09A		
50%(24VDC)	0.3ms or lower	0.3ms or lower	connector ×2		0.16A	64	
	10ms or lower	10ms or lower	_		0.09A		

					Operatin			
Model	Туре	No. of Points	Rated Input Voltage	Input Current	ON voltage	OFF voltage		
A1SX71		32	5/12/24VDC	1.2/3.3/ 7mA	3.5VDC or higher	1VDC or lower		
A1SX80	DC input (sink/source		12/24VDC	3/7mA	8VDC or higher	4VDC or lower		
A1SX80-S1		DC input	16	24VDC	7mA	17VDC or higher	5VDC or lower	
A1SX80-S2				24VDC	71101	13VDC or higher	6VDC or lower	
A1SX81	type)	32	12/24VDC	3/7mA	8VDC or higher	4VDC or lower		
A1SX81-S2		32	24VDC	7mA	13VDC or higher	6VDC or lower		
A1SX82-S1		64	24VDC	5mA	18.5VDC or higher	3VDC or lower		
A1S42X	DC input (dynamic)	*2 16/32 48/64	12/24VDC	4/9mA	8VDC or higher	4VDC or lower		

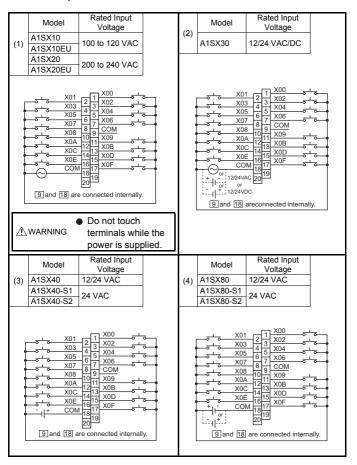
	Maximum	Max. Resp	onse Time			Internal			
	Simultaneous Input Points (Percentage Simultaneously ON)	OFF to ON	ON to OFF	Field Wiring	Points/ Common	Current Con- sumption (5VDC)	NO. of Occupied Points		
	65%(24VDC)	1.5ms or lower	3ms or lower	40-pin connector	32	0.075A	32		
	100%(26.4VDC)	10ms or lower	10ms or lower		16				
	85%(26.4VDC)	0.4ms or lower	0.5ms or lower	Terminal		0.05A	16		
	100%(26.4VDC)	10ms or lower	10ms or lower						
	60%(26.4VDC)	10ms or lower	10ms or lower	37-pin D-sub connector	32	0.08A	32		
	50%(26.4VDC)	0.3ms or lower	0.3ms or lower	40-pin connector ×2	32	0.16A	64		
	100%(26.4VDC)	0.4ms or lower*1	0.4ms or lower*1	24-pin connector	-	0.08A	16/32/48/ 64		

For all modules, the insulation system is photocoupler insulation and the input indications are LED indications.

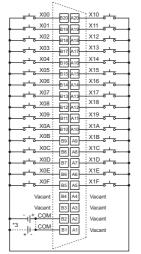
^{*1.....}The dynamic scan cycle is 13.3ms.

^{*2.....}Set using the DIP switch on the module front.

5.1.2 Input module connections



	Model	Rated Input Voltage	
	A1SX41	12/24 VDC	
(E)	A1SX41-S1(S2)	24 VDC	
	A1SX42	12/24 VDC	
	A1SX42-S1(S2)	24 VDC	
	A1SX82-S1*3	24 VDC	

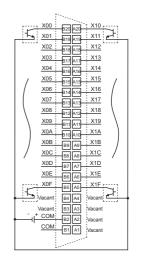


*1 The figure above indicates F (the first half 32 points).

The connections for (the latter half 32 points) are the same as for (regard X00 to X1F as X20 to X3F.)

- B1 and B2 are connected internally.
 The A and B pin number rows shown above are transposed with respect to the diagram of the A and B rows which is printed on the module. Remember that the A row pin numbers correspond to the B row of the module.
- *3 A1SX82-S1 can use both positive common and negative common.

(6)	Model	Rated Input Voltage		
(6)	A1SX71	5/12/24 VDC		



- *1 The figure above shows the connections for the open collector (sink) type.
 [B1] and [B2] are connected internally.
- TTL, LS-TTL, CMOS buffer (sink) connection



· Sensor (source) connection



*2 The A and B pin number rows shown above are transposed with respect to the diagram of the A and B rows which is printed on the module. Remember that the A row pin numbers correspond to the B row of the module.

(7)	Model	Rated Input Voltage	
(7)	A1SX81	12/24 VDC	
	A1SX81-S2	24 VDC	
	A1SX81-S2		X01

	Model	Rated Input	Pin Arrangement	Pin	Signal	Pin	Signal
(8)		Voltage	1 III 7 II rangement	No.	Name	No.	Name
	A1S42X	12/24 VDC		B12	XD0	A12	XD1
				B11	XD2	A11	XD3
			B12 o o A12 B11 o o A11	B10	XD4	A10	XD5
			B10 o o A10	B9	XD6	A9	XD7
			B9 o o A9 B8 o o A8	B8	XSCN0	A8	XSCN1
			B7 o o A7	B7	XSCN2	A7	XSCN3
			B6	B6	XSCN4	A6	XSCN5
			B4 o o A4 B3 o o A3	B5	XSCN6	A5	XSCN7
			B2 o o A2	B4	Vacant	A4	Vacant
			B1 o A1	В3	12/24 VDC	А3	12/24 VDC
			face of the module	B2	0V	A2	0V
				B1	FG	A1	FG
		A X23 X 14 X 13 X 14 X 15 X 16	0		Internal control 1/8th duty		
			re two or more switches				

switch (see right).

5.2 Output modules

5.2.1 Output module specifications

Model	Туре	No. of Points	Rated Load Voltage	Max	Max. Load Current			Max. Output Response Time		
			3 .	Point	Common	Module	OFF to ON	ON to OFF		
A1SY10		16	100V to 240VAC 24VDC			-				
A1SY 10EU	Dolov Ovitovit	10	100V to 120VAC 24VDC	2A	8A	-	10ms or	12ms or		
A1SY 14EU	Relay Output	12	100\/ to	ZA		-	lower	lower		
A1SY 18A		8	100V to 240VAC 24VDC		-	8A				
A1SY 18AEU		0	24VDC		-	-				
A1SY22		16		0.6A	2.4A	-				
A1SY 28A	Triac Output	8	100V to 240VAC	1A	-	8A(AC1 32V, 46°C), 8A(AC2 64V, 40°C) 4A(AC1 32V, 55°C), 2A(AC2 64V, 55°C)	1ms or lower	1ms + 0.5 cycles or less		
A1SY 28EU				0.6A	2.4A (46°C) 1.9A (55°C)	-		1ms + 0.5 cycles or less		

For all modules, the insulation system is photocoupler insulation and the output indications are LED indications.

^{*1.....}The dynamic scan cycle is 13.3ms (FAST mode) or 106.7ms (SLOW mode). (Set using the DIP switch on the module rear.)

^{*2.....}Set using the DIP switch on the module front.

Field Wiring	Points/ Common	Surge Suppression	Fuse Rating	Error display	External Power Supply (TYP DC24V)	Internal Current Consumption	No. of Occupied Points
	8				0.090A	0.12A	
	4	None	None	None	0.1A	0.12A	
					0.075A	0.24A	
	-				0.075A	0.24A	
	8	CR absorber	5A	LED*5	0.002A *3	0.27A	
Terminal	- CR absorber varistor		None	None	-	0.13A	16
	4	CR absorber				0.27A	

^{*3.....}Value at TYP 200VAC.

^{*4.....}Value at TYP 12VDC.

^{*5.....&}quot;ERR." LED turns on when fuse is blown or external supply power is off.

^{*6.....0.08}A is shown on the rating plate of the module.

^{*7.....0.15}A is shown on the rating plate of the module.

Model	Туре	No. of Points	Rated Load Voltage	Max	Max. Load Current			Output se Time	
			· ·	Point	Common	Module	OFF to ON	ON to OFF	
A1SY40						-	2ms or lower	2ms or lower	
A1SY 40P		16			0.8A	-	1ms or lower	1ms or lower	
A1SY41				0.44		-	2ms or lower	2ms or lower	
A1SY 41P		32	12/24V DC	0.1A	2A	-	1ms or lower	1ms or lower	
A1SY42	Output				1.6A	-	2ms or lower	2ms or lower	
A1SY 42P	(sink type)	64			2A	-	1ms or lower	1ms or lower	
A1SY50				0.5A	2A	-			
A1SY60		16	24VDC	2A (25°C) 1.8A (45°C) 1.6A (55°C)	4A (25°C) 3.6A (45°C) 3.2A (55°C)	-	2ms or lower	2ms or lower	
A1SY 60E	Transistor Output (source type)		5/12 /24VDC	2A	4A	-	3ms or	10ms	
A1SY 68A	Transistor Output (sink/source type)	8	5/12 24/ 48VDC	2A	-	-	lower	or lower	
A1SY71	Transistor Output (for TTL/ CMOS) sink type	32	5/ 12VDC	0.016A	0.256A	-	1ms or lower	1ms or lower	

For all modules, the insulation system is photocoupler insulation and the output indications are LED indications.

^{*1.....}The dynamic scan cycle is 13.3ms (FAST mode) or 106.7ms (SLOW mode). (Set using the DIP switch on the module rear.)

^{*2.....}Set using the DIP switch on the module front.

Field Wiring	Points/ Common	Surge Suppression	Fuse Rating	Error display	External Power Supply (TYP DC24V)	Internal Current Consumption	No. of Occupied Points
Terminal	8		1.6A	LED*5	0.008A	0.27A	16
Tomina	0		None	None	0.011A	0.079A*6	10
40-pin			3.2A	LED*5	0.008A	0.5A	32
connector	20		None	None	0.012A	0.141A*7	32
40-pin			3.2A	LED*5	0.008A	0.93A	0.4
connector ×2			None	None	0.014A	0.17A	64
		Zener diode	3.2A		0.06A	0.12A	
Terminal	8		5A	LED*5	0.015A	0.12A	16
remina			7A		0.01A	0.2A	10
	-		None	None	-	0.11A	
40-pin connector	32	None	1.6A	LED*5	0.15A *4	0.4A	32

^{*3.....}Value at TYP 200VAC.

^{*4.....}Value at TYP 12VDC.

^{*5.....&}quot;ERR." LED turns on when fuse is blown or external supply power is off.

^{*6.....0.08}A is shown on the rating plate of the module.

^{*7.....0.15}A is shown on the rating plate of the module.

Model	ndel IVne I		No. of Points Rated Load Voltage		k. Load Cur	rent	Max. (Respon		
				Point	Common	Module	OFF to ON	ON to OFF	
A1SY80		16		0.8A	3.2A	-	2ms or	2ms or	
A1SY81				0.1A	2A	-	lower	lower	
A1SY 81EP	Transistor Output (source type)	32	12/24V	0.1A (25°C), 0.05A (55°C)	2A (25°C) 1.6A (55°C)	-	0.5ms or lower	1.5ms or lower	
A1SY82		64	DC	0.1A	1.6A	-	2ms or lower	2ms or lower	
A1S42Y	Transistor Output (dynamic)	16/32 48/64 *2		0.1A (40°C) 0.05A (55°C)	-	-	2ms or lower *1	2ms or lower *1	

For all modules, the insulation system is photocoupler insulation and the output indications are LED indications.

^{*1} The dynamic scan cycle is 13.3ms (FAST mode) or 106.7ms (SLOW mode). (Set using the DIP switch on the module rear.)

^{*2} Set using the DIP switch on the module front.

Field Wiring	Points/ Common	Surge Suppression	Fuse Rating	Error display	External Power Supply (TYP DC24V)	Internal Current Consumption	No. of Occupied Points
Terminal	8	Zener diode	5A	LED	0.020A	0.12A	16
		Zeriei diode	3.2A	LLD	0.008A	0.5A	
37-pin D-sub connector	32	Clamp diode	None	None	0.080A	0.5A	32
40-pin connector ×2	32	Zener diode	3.2A	LED*5	0.008A	0.93A	64
24-pin connector	-	None	1.6A	LED	0.055A	0.18A	16/32 48/64

^{*3} Value at TYP 200VAC.

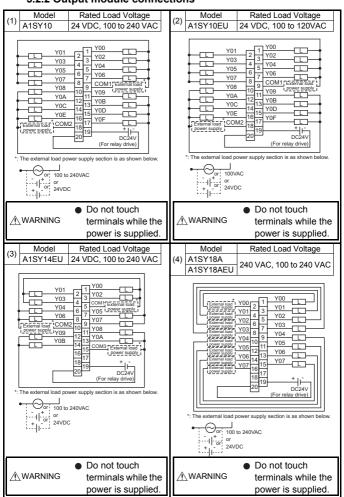
^{*4} Value at TYP 12VDC.

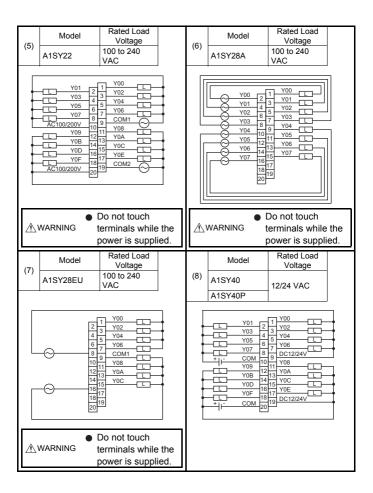
^{*5 &}quot;ERR." LED turns on when fuse is blown or external supply power is off.

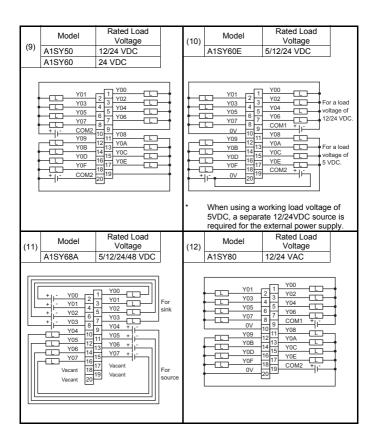
^{*6 0.08}A is shown on the rating plate of the module.

^{*7 0.15}A is shown on the rating plate of the module.

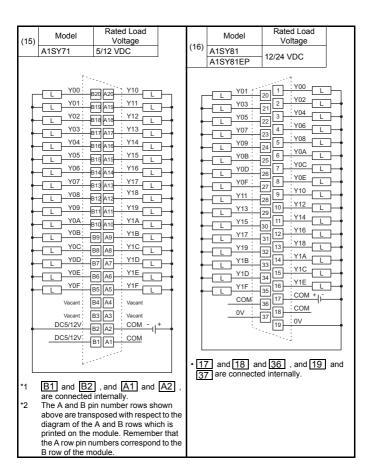
5.2.2 Output module connections







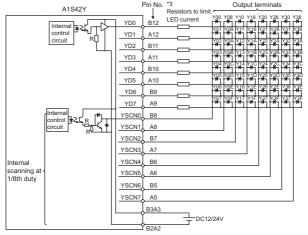
(40)	Model	Rated Load Voltage			Model	Rated Load Voltage
(13)	A1SY41 A1SY41P	12/24 VDC		(14)	A1SY42 A1SY42P	12/24 VDC
	L Y01 L Y02 L Y03 L Y06 L Y06 L Y06 L Y07 L Y07 L Y08 L Y08 L Y09 L Y00 L Y00 L Y00 L Y0C	220 A220 Y10 L 119 A19 Y11 L 119 A19 Y11 L 119 A18 Y12 L 117 A17 Y13 L 117 A17 Y14 L 115 A15 Y15 L 114 A14 Y16 L 113 A13 Y17 L 114 A14 Y16 L 115 A15 Y17 L 116 A16 Y17 L 117 A17 Y18 L 118 A18 Y17 L 118 A19 Y18 L 119 A10 Y1A L 1			L Y00 82 81 1	8 A18
*1	are connected The A and B p above are trans diagram of the printed on the	in number rows sh sposed with respec A and B rows whi module. Remembe umbers correspond	own t to the ch is er that	*1	half 32 points) The connectio 32 points) are (regard Y00 to B1 and B2 are connected The A and B p above are tran- diagram of the printed on the	ns for \(\backsquare\) (the latter half the same as for \(\backsquare\) (T) F as Y20 to Y3F.) \(\), and \(\backsquare\) and \(\backsquare\) (A1) and \(\backsquare\) (A2), internally. in number rows shown sposed with respect to the A and B rows which is module. Remember that umbers correspond to the



(17)	N	1odel			ted Load 'oltage
,,	A1SY	′82	-	2/24	
		V00	٠	· · · · · ·	
	L	Y00	B20	A20	Y10 L
+	L	Y01	B19	A19	Y11 L
+	L	Y02:	B18	A18	Y12 L
\downarrow	$\overline{}$	Y03	B17	A17	Y13
		Y04	B16	A16	Y14 T
		Y05	B15	A15	Y15
		Y06			V16
•	<u></u>	Y07	B14	A14	Y17
_	L	Y08	B13	A13	Y18
+	L	-	B12	A12	
+	L	Y09	B11	A11	; Y19
+	L	Y0A	B10	A10	Y1A L
\perp	-	Y0B	B9	A9	Y1B L
\perp	$\overline{}$	Y0C	B8	 A8	Y1C
L		Y0D	B7	A7	: Y1D
I		Y0E	B6][A6]	Y1E
	<u> </u>	Y0F	\vdash		V1E
Ť		1	B5	A5	
		Vacant	В4	A4	Vacant
12	/24VDC		В3	А3	Vacant
+	⊣⊢	СОМ	B2	A2	: 0V
	_	СОМ	B1	A1	: 0V
		1			.2
		į.			
- ∟ •1	The	:	h a	n indi	cates FI (the

- *1 The figure above indicates F (the first half 32 points).
 The connections for L (the latter half
 - The connections for $\[\]$ (the latter half 32 points) are the same as for $\[\]$ (regard Y00 to Y1F as Y20 to Y3F.) $\[\]$ $\[\]$ $\[\]$ $\[\]$ $\[\]$ $\[\]$ $\[\]$ $\[\]$ $\[\]$ $\[\]$ $\[\]$ and $\[\]$ $\[\]$ $\[\]$ $\[\]$ and $\[\]$ $\[\]$ $\[\]$ are connected internally.
- *2 The A and B pin number rows shown above are transposed with respect to the diagram of the A and B rows which is printed on the module. Remember that the A row pin numbers correspond to the B row of the module.

(18)	Model	Rated Load Voltage	Pin Arrangement	Pin No.	Signal Name (F _H)	Pin No.	Signal Name (F _H)
	A1S42Y	12/24 VDC		B12	YD0	A12	YD1
		•	B12 0 0 A12	B11	YD2	A11	YD3
			B11 0 0 A11	B10	YD4	A10	YD5
			B10 • • A10 B9 • • A9	В9	YD6	A9	YD7
			B8 • • A8	B8	YSCN0	A8	YSCN1
			B7 0 0 A7 B6 0 0 A6	B7	YSCN2	A7	YSCN3
			B5 0 0 A5	B6	YSCN4	A6	YSCN5
			B4 0 0 A4 B3 0 0 A3	B5	YSCN6	A5	YSCN7
			B2 0 0 A2 B1 0 0 A1	B4	Vacant	A4	Vacant
			Seen from front	В3	12/24 VDC	А3	12/24 VDC
			face of the module	B2	0V	A2	0V
				B1	Vacant	A1	Vacant



- *1 The fuse in the output module is provided to prevent the external wiring from burning in the event of a short circuit in the module's output. Consequently, it may not be able to protect output devices. If an output device is damaged in a failure mode other than a short circuit.
- the fuse might not be blown. The "ERR." LED will also come ON when the external power supply is cut.
- *4

A1S42Y.

*3

The power supply voltage (12/24VDC) is applied in the LED's reverse direction. If the peak inverse voltage is insufficient, connect protective diodes in series with each of the LEDs.

Mount the resistors to limit

LED current externally to the

MEMO

5.3 Input/output combined modules

5.3.1 Input/output combined module specifications

		No. of		lanus	Operating	g Voltage	
Model	Type	Points	Rated Input Voltage	Input Current	ON Voltage	OFF	
					orr romage	Voltage	
A1SH42			12/24VDC	2/5mA	8VDC or	4VDC or	
A1SH42P		32	12/24100	2/3IIIA	higher	lower	
A1SH42-S1	DC Input	32	24VDC	5mA	15VDC or	3VDC or	
A1SH42P-S1	(sink type)		24700	SIIIA	higher	lower	
A1SX48Y18		8	24VDC	7mA	14VDC or	6.5VDC or	
A1SX48Y58		٥	24700	IIIA	higher	lower	

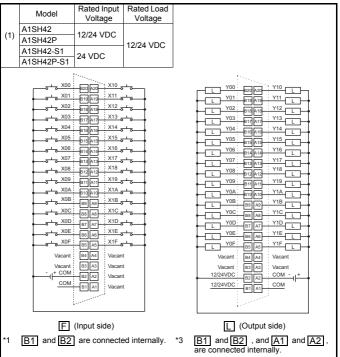
Model	Туре	No. of Points	Rated Load	Max. Loa	ad Current	Max. Output Response Time		
			Voltage	1 Point	Common	OFF to ON	ON to OFF	
A1SH42					1.6A	2ms or lower	2ms or lower	
A1SH42P	Transistor	32	12/	0.1A	2A	1ms or lower	1ms or lower	
A1SH42-S1	Output (sink type)	32	24VDC	0.1A	1.6A	2ms or lower	2ms or lower	
A1SH42P-S1					2A	1ms or lower	1ms or lower	
A1SX48Y18	Relay Output		24VDC 240VAC	2A	8A	10ms or lower	12ms or lower	
A1SX48Y58	Transistor Output (sink type)	8	12/ 24VDC	0.5A	2A	2ms or lower	2ms or lower	

For all modules, the insulation system is photocoupler insulation and the operation status is provided by LED indications.

Max. Simultaneous Input Points	Max. Response Time		No. of
(Percentage Simultaneously ON)	OFF to ON	ON to OFF	Occupied Points
60%(24VDC)	10ms or lower	10ms or lower	- 32
	0.3ms or lower	0.3ms or lower	
100%(26.4VDC)	10ms or lower	10ms or lower	8

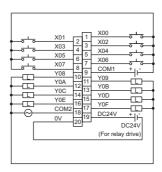
Points/ Common	Field Wiring	Surge Suppression	Fuse Rating	External Power Supply (TYP 24VDC) Current	External Current Consumption (5VDC)	No. of Occupied Points
32	40-pin Connector	Zener diode	3.2A	0.008A	0.5A	32
			None	0.012A	0.13A	
			3.2A	0.008A	0.5A	
			None	0.012A	0.13A	
8	Terminal	None	None	0.045A	0.085A	16
		Zener diode	3.2A	0.06A	0.06A	

5.3.2 Input/output composite module connections



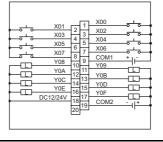
^{*2} The A and B pin number rows shown above are transposed with respect to the diagram of the A and B rows which is printed on the module. Remember that the A row pin numbers correspond to the B row of the module.

(2)	Model	Rated Input Voltage	Rated Load Voltage
(2)	A1SX48Y18	24 VDC	24 VDC/ 240 VAC



♠ WARNING ■ Do not touch terminal while the power is supplied.

(3)	Model	Rated Input Voltage	Rated Load Voltage
` ′	A1SX48Y58	24 VDC	12/24 VDC



6. FRROR CODES

When an error occurs while in the PLC RUN or RUN state, the self-diagnostic function stores an error indication or error code (including step number) in the special register. This section describes how to read the error code, as well as causes of errors and corrective actions.

6.1 How to read error codes

When an error occurs, the error code can be read by peripheral equipment.

For handling of the equipment, refer to the operating manuals for peripheral equipment.

- 6.2 Error Code List for A1SCPUC24-R2 (Table 6.1)
- 6.3 Error Code List for A1SHCPU and A2SHCPU (Table 6.2)
- 6.4 Error Code List for A2USCPU(S1) and A2USHCPU-S1 (Table 6.3) Take a proper action and eliminate the cause of error.

6.2 Error Code List for A1SCPUC24-R2

Table 6.1 Error Code List for A1SCPUC24-R2

Error Message	Error Code (D9008)	CPU States	Error and Cause	Corrective Action
"INSTRCT. CODE ERR" (Checked at the execution of instruction)	10	Stop	Instruction code, which cannot be decoded by CPU, is included in the program. (1) EP-ROM or memory cassette, which cannot be decoded, has been loaded. (2) Since the memory contents have changed for some reason, instruction code, which cannot be decoded, has been included.	(1) Read the error step by use of a peripheral equipment and correct the program at that step. (2) In the case of EP-ROM or memory cassette, rewrite the contents or replace with an EP-ROM or memory cassette which stores correct contents.
"PARAMETER ERROR" (Checked at power-on, STOP → RUN, and PAUSE → RUN)	11	Stop	(1) Capacity larger than the memory capacity of CPU module has been set with the peripheral equipment and then write to CPU module has been performed. (2) The contents of parameters of CPU memory have changed due to noise or the improper loading of memory. (3) RAM is not loaded to the A1 or A1NCPU.	(1) Check the memory capacity of CPU with the memory capacity set by peripheral equipment and re-set incorrect area. (2) Check the loading of CPU memory and load it correctly. Read the parameter contents of CPU memory, check and correct the contents, and write them to CPU again. (3) Install the RAM and write parameter contents from a peripheral device.
"MISSING END INS." (Checked at STOP → RUN)	12	Stop	(1) There is no END (FEND) instruction in the program. (2) When subprogram has been set by the parameter, there is no END instruction in the subprogram.	Write END instruction at the end of program.

Table 6.1 Error Code List for A1SCPUC24-R2 (Continue)

Error Message	Error Code (D9008)	CPU States	Error and Cause	Corrective Action
"CAN'T EXECUTE(P)"	13	Stop	(1) There is no jump destination or multiple destination or multiple destinations specified by the [CJ], SCJ], [CALL], Or [Jmp] instruction. (2) There is a CHG instruction and no setting of subprogram. (3) Although there is no [CALL] instruction, the RET] instruction exists in the program and has been executed. (4) The [CJ], [SCJ], [CALL], CALLP], or [Jmp] instruction has been executed with its jump destination located below the END instruction. (5) The number of the FOR instructions is different from that of the NEXT] instructions. (6) A [Jmp] instruction is given within a FORD sext] loop causing the processing to exit the loop. (7) Processing exited subroutine by the JMP] instruction of the RET] instruction of the RET] instruction. (8) Processing jumped into a step in a FORD sext] loop or into a subroutine by the JMP] instruction. (9) The STOP instruction is given in an interrupt program, a subroutine program or in a FORD sext] loop.	Read the error step by use of peripheral equipment and correct the program at that step. (Insert a jump destination or reduce multiple destinations to one.)

Table 6.1 Error Code List for A1SCPUC24-R2 (Continue)

Error Message	Error Code (D9008)	CPU States	Error and Cause	Corrective Action
"CHK FORMAT ERR" (Checked at STOP/PAUSE → RUN)	14	Stop	(1) Instructions (including NoP) except LD Xº; LDI Xº; AND Xº; and ANI Xº; are included in the CHK instruction circuit block. (2) Multiple CHK instructions are given. (3) The number of contact points in the CHK instruction circuit block exceeds 150. (4) There is no ⊢□Rº; circuit block before the CHK instruction circuit block. (5) The device number of D1 of the CHK D1D2 instruction is different from that of the contact point before the CJ Pº; instruction. (6) Pointer P254 is not given to the head of the CHK instruction circuit block. P254+eee-CHK D1D2-	Check the program in the ⊙HK instruction circuit block according to items (1) to (6) in the left column. Correct problem using the peripheral and perform operation again.

Table 6.1 Error Code List for A1SCPUC24-R2 (Continue)

Error Message	Error Code (D9008)	CPU States	Error and Cause	Corrective Action
"CAN'T EXECUTE (I)" (Checked at the occurrence of interruption)	15	Stop	(1) Although the interrupt module is used, there is no number of interrupt pointer I, which corresponds to that module, in the program or there are multiple numbers. (2) No [RET] instruction has been entered in the interrupt program. (3) There is [RET] instruction in other than the interrupt program.	(1) Check for the presence of interrupt program which corresponds to the interrupt unit, create the interrupt program, and reduce the same numbers of I. (2) Check if there is IRET instruction in the interrupt program and enter the IRET instruction. (3) Check if there is IRET instruction in other than the interrupt program and elete the IRET instruction in other than the interrupt program and delete the IRET instruction.
"CASSETTE ERROR" (Checked at power-on) An, AnN only	16	Stop	The memory cassette is not loaded.	Turn off the power, insert the memory cassette and turn on the power again.
"ROM ERR"	17	Stop	Parameters and/or sequence programs are not correctly written to the mounted memory cassette.	Correctly write parameters and/or sequence programs to the memory cassette. Remove the memory cassettes that contain no parameters or sequence programs.
			Parameters stored in the memory cassette have exceeded the limit of available program capacity. Ex.) Default parameters (program capacity: 6k steps) are written to A1NMCA-2KE.	Adjust the program capacity for parameters to the memory cassette used. Use the memory cassette of which memory capacity is larger than the program capacity for parameters.

Table 6.1 Error Code List for A1SCPUC24-R2 (Continue)

Error Message	Error Code (D9008)	CPU States	Error and Cause	Corrective Action
"RAM ERROR" (Checked at power-on)	20	Stop	The CPU has checked if write and read operations can be performed properly to the data memory area of CPU, and as a result, either or both has not been performed.	Since this CPU hardware error, consult Mitsubishi
"OPE. CIRCUIT ERR" (Checked at power-on)	21	Stop	The operation circuit, which performs the sequence processing in the CPU, does not operate properly.	representative.
"WDT ERROR" (Checked at the execution of END processing)	22	Stop	Scan time exceeds watch dog error monitor time. (1) Scan time of user program has been exceeded for some conditions. (2) Scan time has lengthened due to instantaneous power failure which occurred during scan.	(1) Calculate and check the scan time of user program and reduce the scan time using the [Ca] instruction or the like. (2) Monitor the content of special register D9005 by use of peripheral equipment. When the content is other than 0, line voltage is insufficient. When the content is other than 0, the power voltage is unstable.
"SUB-CPU ERROR" (Checked continuously)	23 (During run) 26 (At power- on)	Stop	Sub-CPU is out of control or defective.	Since this CPU hardware error, consult Mitsubishi representative.
"END NOT EXECUTE" (Checked at the execution of END instruction)	24	Stop	(1) When the END instruction was to be executed, the instruction was read as other instruction code due to noise or the like. (2) The END instruction has changed to another instruction code for some reason.	Perform reset and run. If the same error is displayed again, it is the CPU hardware error, consult Mitsubishi representative.

Table 6.1 Error Code List for A1SCPUC24-R2 (Continue)

Error Message	Error Code (D9008)	CPU States	Error and Cause	Corrective Action
"WDT ERROR" (Checked continuously)	25	Stop	The CPU is executing an endless loop.	Since the program is in an endless lop due to the JMP and CJ instructions, check the program.
"MAIN CPU DOWN" (Checked continuously)	26	Stop	Main-CPU is out of control or defective. (Sub-CPU checked it.)	Since this is a CPU hardware error, consult Mitsubishi representative.
"UNIT VERIFY ERR. "	31	Stop or Continue (set by para- meter)	I/O module data are different from those at power-on. The I/O module (including the special function module) is incorrectly loaded or has been removed, or a different unit has been loaded.	(1) Among special registers D9116 to D9123, the bit corresponding to the module of verify error is "1". Therefore, use peripheral equipment to monitor the registers and check for the module with "1" and make replacement. (2) When the present unit arrangement is OK, perform reset with the reset switch.

Table 6.1 Error Code List for A1SCPUC24-R2 (Continue)

Error Message	Error Code (D9008)	CPU States	Error and Cause	Corrective Action
"FUSE BREAK OFF" (Checked continuously)	32	Stop or Continue (set by para- meter)	A fuse is blown in an output modul. The external output supply for output load is not turned off or not connected.	(1) Check the fuse blown indicator LED of output module and change the fuse of module of which LED is on. (2) Among special registers D9100 to D9107, the bit corresponding to the unit of fuse break is "1" Replace the fuse of a corresponding module. Monitor and check it. (3) Check if the external power supply for output load is turned on or off.
"CONTROL-BUS ERR." (Checked at the execution of FROM and TO instructions)	40	Stop	The FROM and TO instructions can-not be executed. Error of control bus with special function module.	Since this is a hardware error of a special function module, CPU module, or base unit, replace the module and check the defective module, consult Mitsubishi representative.
"SP. UNIT DOWN" (Checked at the execution of FROM and TO instructions.)	41	Stop	When the FROM or TO instruction is executed, access has been made to the special function module but the answer is not given. The accessed special function module is defective.	Since this is an accessed special function module error, consult Mitsubishi representative.
"LINK UNIT ERROR"	42	Stop	The data link module is loaded in the master station.	Remove the data link module from the master station. After correction, reset and start from the initialization.

Table 6.1 Error Code List for A1SCPUC24-R2 (Continue)

Error Message	Error Code (D9008)	CPU States	Error and Cause	Corrective Action
"I/O INT. ERROR"	43	Stop	Although the interrupt module is not loaded, interruption has occurred.	Since this is a hardware error of a specific module, replace the module and check the defective module, consult Mitsubishi representative.
"SP. UNIT LAY. ERROR."	44	Stop	(1) Three or more computer link units are loaded with respect to one CPU module. (A1SCPU24-R2 is also counted as one unit.) (2) Two or more data link modules are loaded. (3) Two or more interrupt units are loaded. (4) A special function module is assigned in place of an I/O module, or vice versa, at I/O assignment of parameters on peripheral devices. (5) The input/output modules or special function modules or special function modules or special function modules or of parameters on peripheral devices. (5) The input/output modules or special function modules or special function modules or of special function modules or of special function modules or special function	(1) Reduce the computer link modules to two or less. (2) Reduce the data link modules to one or less. (3) Reduce the interrupt module to one. (4) Re-set the I/O assignment of parameter setting by use of peripheral devices according to the actually loaded special function module. (5) Review the input/output numbers, and remove the modules at the input/output numbers beyond the number of input/output points or GOT.
"SP. UNIT ERROR" (Checked at the execution of FROM and TO instructions)	46	Stop or Continue (set by para- meter)	Access (execution of FROM to To instruction) has been made to a location where there is not special function unit.	Read the error step by use of peripheral equipment, and check and correct the content of FROM or [TO] instruction at that step.

Table 6.1 Error Code List for A1SCPUC24-R2 (Continue)

Error Message	Error Code (D9008)	CPU States	Error and Cause	Corrective Action
"LINK PARA. ERROR"	47	Continue	(1) If a data link CPU is used to set a master station (station number "00"): The contents written to the parameter area of link by setting the link range in the parameter setting of peripheral devices are different from the link parameter contents for some reason. Or, link parameters are not written. (2) The setting of the total number of slave stations is 0.	(1) Write parameters again and make check. (2) Check setting of station numbers. (3) When the error is displayed again, it is hardware error. Therefore, consult Mitsubishi representative.
"OPERATION ERROR" (Checked during execution of instruction)	50	Continue	(1) The result of BCD conversion has exceeded the specified range (9999 or 99999999). (2) Operation impossible because specified device range has been exceeded. (3) File registers used in program without capacity setting. (4) Operation error occurred during execution of the RTOP, RFRP, LWTP or LRDP instruction.	Read the error step using peripheral devices and check the program at the error step, and correct it. (Check the specified device range, BCD conversion, or the like.)
"MAIN CPU DOWN" (Interrupt fault) AnNCPU only	60	Stop	(1) INT instruction processed in microcomputer program area. (2) CPU malfunction due to noise. (3) Hardware error of CPU module.	(1) Because the INT instruction cannot be used in the microcomputer program, remove it. (2) Take measures against noises. (3) Consult Mitsubishi representative.

Table 6.1 Error Code List for A1SCPUC24-R2 (Continue)

Error Message	Error Code (D9008)	CPU States	Error and Cause	Corrective Action
"BATTERY ERROR" (Checked at power-on)	70	Continue	(1) The battery voltage has dropped to below the specified value.(2) The lead connector of the battery is not connected.	Replace battery. Connect the lead connector if RAM memory or power failure compensation function is used.

6.3 Error Code List for A1SHCPU and A2SHCPU

The following are the explanation about the descriptions and the causes of the error messages, error codes and the detailed error codes, and their correctives actions.

The detailed error codes are stored in D9092 only when using the dedicated instruction for CC-Link.

Table 6.2 Error Code List for A1SHCPU and A2SHCPU

Error Message	Error Code (D9008)	Detailed Error Code (D9092)	CPU States	Error and Cause	Corrective Action
"INSTRCT. CODE ERR"	10	_		Instruction code, which cannot be decoded by CPU module, is included in the program. (1) Memory cassette including instruction code, which cannot be decoded, has been loaded. (2) Since the memory contents have changed for some reason, instruction code, which cannot be decoded, has been included.	(1) Read the error step by use of peripheral equipment and correct the program at that step. (2) In the case of memory cassette, rewrite the contents or replace the cassette with a memory cassette which stores correct contents.
(Checked at STOP→ RUN.)	10	101	Stop	Instruction code, which cannot be decoded by CPU module, is included in the program. (1) Memory cassette including instruction code, which cannot be decoded, has been loaded. (2) Since the memory contents have changed for some reason, instruction code, which cannot be decoded, has been included.	(1) Read the error step by use of peripheral equipment and correct the program at that step. (2) In the case of memory cassette, rewrite the contents or replace the cassette with a memory cassette which stores correct contents.

Table 6.2 Error Code List for A1SHCPU and A2SHCPU (Continue)

Error Message	Error Code (D9008)	Detailed Error Code (D9092)	CPU States	Error and Cause	Corrective Action
"INSTRCT. CODE ERR"		103		Device specified by a dedicated instruction for CC-Link is not correct.	
	10	104	Stop	A dedicated instruction for CC-Link has incorrect program structure.	Read the error step using a peripheral device and correct the program of the step.
(Checked at STOP→ RUN.)		105		A dedicated instruction for CC-Link has incorrect command name.	
"PARAMETE R ERROR" (Checked at power-on or STOP/ PAUSE → RUN.)	11	_	Stop	The contents of parameters of CPU memory have changed due to noise or the improper loading of memory.	(1) Load the memory cassette correctly. (2) Read the parameter contents of CPU memory, check and correct the contents, and write them to CPU again.
"MISSING END INS." (Checked at STOP→ RUN.)	12	_	Stop	There is no END (FEND) instruction in the program.	Write END instruction at the end of program.

Table 6.2 Error Code List for A1SHCPU and A2SHCPU (Continue)

Error Message	Error Code (D9008)	Detailed Error Code (D9092)	CPU States	Error and Cause	Corrective Action
"CAN'T EXECUTE (P)"	13		Stop	(1) There is no jump destination or multiple destinations specified by the [CJ], [SCJ], [CALL], or [JMP] instruction. (2) Although there is no [CALL] instruction, the RET] instruction exists in the program and has been executed. (3) The [CJ], [SCJ], [CALL], or [JMP] instruction has been executed with its jump destination located below the [END] instructions. (4) The number of the FOR instructions is different from that of the NEXT] instructions is given within a [FOR ID NEXT] loop causing the processing to exit the loop. (6) Processing exited subroutine by the JMP] instruction. (7) Processing jumped into a step in a [FOR ID NEXT] loop or into a subroutine by the JMP] instruction.	Read the error step by use of peripheral equipment and correct the program at that step. (Insert a jump destination or reduce multiple destinations to one.)

Table 6.2 Error Code List for A1SHCPU and A2SHCPU (Continue)

Error Message	Error Code (D9008)	Detailed Error Code (D9092)	CPU States	Error and Cause	Corrective Action
"CHK FORMAT ERR" (Checked at STOP/ PAUSE → RUN.)	14	_	Stop	(1) Instructions (including NOP) except LD X□, LDI X□, AND X□ and ANI X□ are included in the OHK instruction circuit block. (2) Multiple CHK instructions are given. (3) The number of contact points in the CHK instruction circuit block exceeds 150. (4) The device number of X in the CHK instruction circuit block exceeds X7FE. (5) There is no HOLE ALI CHK Instruction circuit block exceeds X7FE. (6) The device number of D1 of the CHK instruction circuit block. (6) The device number of D1 of the CHKDID2 instruction is different from that of the contact point before the CJP□ instruction. (7) Pointer P254 is not given to the head of the ICHK instruction circuit block. P254 → CHKDID2 instruction circuit block.	(1) Check the program in the CHK instruction circuit block according to item (1) to (7) in the left column. Correct problem using the peripheral equipment and perform operation again. (2) This error code is only effective when the input/output control method is a direct method.

Table 6.2 Error Code List for A1SHCPU and A2SHCPU (Continue)

Error Message	Error Code (D9008)	Detailed Error Code (D9092)	CPU States	Error and Cause	Corrective Action
"CAN'T EXECUTE (I)"	15		Stop	(1) Although the interrupt module is used, there is no number of interrupt pointer I, which corresponds to that module, in the program or there are multiple numbers. (2) No IRET instruction has been entered in the interrupt program. (3) There is IRET instruction in other than the interrupt program.	(1) Check for the presence of interrupt program which corresponds to the interrupt unit, create the interrupt program, and reduce the same numbers of I. (2) Check if there is IRET instruction in the interrupt program and enter the IRET instruction. (3) Check if there is IRET instruction in other than the interrupt program and elete the IRET instruction in other than the interrupt program and delete the IRET instruction.
"ROM ERR"	17		Stop	Parameters and/or sequence programs are not correctly written to the mounted memory cassette. Parameters stored in the memory cassette have exceeded the limit of available program capacity. Ex.) Default parameters (program capacity: 6k steps) are written to	(1) Correctly write parameters and/or sequence programs to the memory cassette. (2) Remove the memory cassettes that contain no parameters or sequence programs. (1) Adjust the program capacity for parameters to the memory cassette used. (2) Use the memory cassette of which memory capacity is larger than the program capacity
power-on.)				A1NMCA-2KE.	for parameters.

Table 6.2 Error Code List for A1SHCPU and A2SHCPU (Continue)

Error Message	Error Code (D9008)	Detailed Error Code (D9092)	CPU States	Error and Cause	Corrective Action
"RAM ERROR" (Checked at power-on.)	20	_	Stop	The CPU has checked if write and read operations can be performed properly to the data memory area of CPU, and as a result, either or both has not been performed.	Since this CPU hardware error, consult Mitsubishi representative.
"OPE. CIRCUIT ERR" (Checked at power-on.)	21	_	Stop	The operation circuit, which performs the sequence processing in the CPU, does not operate properly.	
"WDT ERROR" (Checked at END instruction.)	22	_	Stop	Scan time exceeds watch dog error monitor time. (1) Scan time of user program has been exceeded for some conditions. (2) Scan time has lengthened due to instantaneous power failure which occurred during scan.	(1) Calculate and check the scan time of user program and reduce the scan time using the [GJ] instruction or the like. (2) Monitor the content of special register D9005 by use of peripheral equipment. When the content is other than 0, line voltage is insufficient. When the content is other than 0, the power voltage is unstable.

Table 6.2 Error Code List for A1SHCPU and A2SHCPU (Continue)

Error Message	Error Code (D9008)	Detailed Error Code (D9092)	CPU States	Error and Cause	Corrective Action
"END NOT EXECUTE" (Checked at END processing.)	24	ı	Stop	(1) When the END instruction was to be executed, the instruction was read as other instruction code due to noise or the like. (2) The END instruction has changed to another instruction code for some reason.	Reset and run the CPU module again. If the same error is displayed again, it is the CPU hardware error, consult Mitsubishi representative.
"WDT ERROR"	25	_	Stop	The CJ instruction or the like causes a loop in execution of the sequence program to disable execution of the END instruction.	Check the program for an endless loop and correct.
"UNIT VERIFY ERR."	31	_	Stop or Continue (set by para- meter)	I/O module data are different from those at power-on. (1) The I/O module (including the special function module) is incorrectly loaded or has been removed, or a different unit has been loaded.	(1) The bit in special registers D9116 to D9123 corresponding to the module causing the verification error is "1." Use a peripheral device to monitor the registers to locate the "1" bit, and check or replace the corresponding module. (2) To accept the current module arrangement, operate the RUN/STOP key switch to reset.

Table 6.2 Error Code List for A1SHCPU and A2SHCPU (Continue)

Error Message	Error Code (D9008)	Detailed Error Code (D9092)	CPU States	Error and Cause	Corrective Action
"FUSE BREAK OFF"	32	_	Stop or Continue (set by para- meter)	(1) The fuse is blown in some output modules. (2) The external power supply for the output load is turned off or it is disconnected.	(1) Check the ERR LED of the output module. Replace the module with the lit LED. (2) Among special registers D9100 to D9107, the bit corresponding to the unit of fuse break is "1" Replace the fuse of a corresponding module. Monitor and check it. (3) Check ON/OFF of the external power supply for the output load.
"CONTROL- BUS ERR."	40	_	Stop	The FROM and TO instructions cannot be executed. (1) Error of control bus with special function module.	The hardware of the special function module, CPU module or base unit is faulty. Replace the faulty module and check the faulty module. Consult Mitsubishi representative.
"SP. UNIT DOWN"	41	_	Stop	There is no reply from the special function module during execution of the FROM or TO instruction. (1) The special function module being accessed is faulty.	The hardware of the special function module being accessed is faulty. Consult Mitsubishi representative.
"I/O INT. ERROR"	43	_	Stop	Interrupt occurs though no interrupt module is installed.	The hardware of a module is faulty. Replace the module and check the faulty module. Consult Mitsubishi representative.

Table 6.2 Error Code List for A1SHCPU and A2SHCPU (Continue)

Error Message	Error Code (D9008)	Detailed Error Code (D9092)	CPU States	Error and Cause	Corrective Action
"SP. UNIT LAY. ERROR."	44		Stop	(1) Three or more computer link modules are installed for a single CPU module. (2) Two or more MELSECNET (II), MELSECNET /B or MELSECNET /710 data link modules are installed. (3) Two or more interrupt modules are installed. (4) A special function module is installed to a slot assigned to the I/O module with parameter setup of the peripheral device, or vice versa. (5) The I/O module or special function module is installed outside the following I/O number ranges, or GOT is connected to the bus. A1SH, A1SJHCPU: X0 to XFF A2SHCPU(S1): X0 to X1FF	(1) Reduce the number of computer link modules to within two. (2) Reduce the number of MELSECNET (II), MELSECNET /B and MELSECNET /710 data link modules to one. (3) Reduce the number of interrupt modules to one. (4) Using the peripheral device, correct the parameter I/O assignment according to the actual state of installation of the special function modules. (5) Examine the I/O number and remove the modules and GOT installed outside the range specified on the left.

Table 6.2 Error Code List for A1SHCPU and A2SHCPU (Continue)

Error Message	Error Code (D9008)	Detailed Error Code (D9092)	CPU States	Error and Cause	Corrective Action
"SP. UNIT ERROR"		_		(1) Access (execution of FROM or [TO] instruction) has been made to a location where no special function module is installed.	(1) Use the peripheral device to read and correct the FROM and/or [TO] instruction at the error step.
(Checked at FROM/TO instructions or a special function module dedicated instruction.)	46	462	Stop or Continue (set by para- meter)	(1) There is inconsistency in the module name between the special instruction for CC-Link and I/O assignment of the parameter. (2) The location designated by the special instruction for CC-Link is not the master module.	(1) Correct the module name of I/O assignment of the parameter to that of the special instruction for CC-Link. (2) Use the peripheral device to check and correct the special instruction for CC-Link at the error step.
"LINK PARA. ERROR"	47	_	Continue	(1) There is inconsistency for some reason between the data, which is written by the peripheral device in the parameter area of the link under link range designation using parameter setup, and the link parameter data read by the CPU module. (2) The total number of stations is set at "0."	(1) Write parameters and check again. (2) If the error persists, there is a fault in hardware. Consult Mitsubishi representative.

Table 6.2 Error Code List for A1SHCPU and A2SHCPU (Continue)

Error Message	Error Code (D9008)	Detailed Error Code (D9092)	CPU States	Error and Cause	Corrective Action	
"OPERATION ERROR"	50	_	Stop or Continue (set by para- meter)	Continue	(1) The result of BCD conversion exceeds the rated range ("9999" or "9999999"). (2) There is a setting exceeding the rated device range, disabling execution of calculation. (3) The file register is used on the program without designation of the capacity of the file register.	Use the peripheral device to read and correct the error step in the program. (Check the setting range of the device, BCD conversion value and so on.)
	50	ı,		meter) The data stored by the designated device or a constant exceeds the allowable range.	Use the peripheral device to read and correct the error step	
		504		The setting quantity of handled data exceeds the allowable range.	in the program.	
					The number of special instructions for CC-Link executed in each scan exceeds 64.	Reduce the special instructions for CC-Link executed in each scan to within 64.
(Checked at instruction execution.)		509		A special instruction for CC-Link is executed to a CC-Link module to which no parameter is defined.	Define parameters.	
"MAIN CPU DOWN"	60	_	Stop	(1) The CPU walfunctioned due to noise. (2) Hardware failure.	Take proper countermeasures for noise. Consult Mitsubishi representative.	
"BATTERY ERROR" (Always checked.)	70	_	Continue	(1) The battery voltage is low. (2) The battery lead connector is not connected.	Replace the battery. Connect the lead connector to use the built-in RAM memory or power failure compensation function.	

6.4 Error Code List for A2ASCPU(S1/S30) and A2USHCPU-S1

Error codes are generated as follows:

Table 6.3 Error Code List for A2ASCPU(S1/S30) and A2USHCPU-S1

Error Massage	Error Code (D9008)	Detailed Error Code (D9091)	CPU States	Error and Cause	Corrective Action
"INSTRCT CODE ERR"	10	101	STOP	Instruction codes which the CPU cannot decode are included in the program.	(1) Read the error step using a peripheral device and correct the program of the step. (2) Check the ROM if it contains instruction codes which cannot be decoded. If it does, replace it with a correct ROM.
	10	102		Index qualification is specified for a 32-bit constant.	
		103		Device specified by a dedicated instruction is not correct.	
		104		An dedicated instruction has incorrect program structure.	Read the error step using a peripheral device and correct
(Checked		105		An dedicated instruction has incorrect command name.	the program of the step.
when STOP → RUN or at execution of instruction.)		106		Index qualification using Z or V is included in the program between LEDA/BIX and LEDA/BIXEND.	

Table 6.3 Error Code List for A2ASCPU(S1/S30) and A2USHCPU-S1 (Continue)

Error Massage	Error Code (D9008)	Detailed Error Code (D9091)	CPU States	Error and Cause	Corrective Action
"INSTRCT CODE ERR" (Checked when STOP —> RUN or at execution of	10	107	STOP	(1) Index qualification is specified for the device numbers and set values in the out instruction of timers and counters. (2) Index qualification is specified at the label number of the pointer (P) provided to the head of destination of the [cJ], [SCJ], [CALL], [CALLP, [JMP], LEDA/BJECALL] and LEDA/BJECALL and LeDA/BJECALL and Ledel number of the interrupt pointer (I) provided to the head of an interrupt program. Errors other than 101 to	Read the error step using a peripheral device and correct the program of the step.
instruction.)		108		107 mentioned above.	

Table 6.3 Error Code List for A2ASCPU(S1/S30) and A2USHCPU-S1 (Continue)

Error Massage	Error Code (D9008)	Detailed Error Code (D9091)	CPU States	Error and Cause	Corrective Action		
"PARA- METER ERROR"	METER	111	STOP		Capacity settings of the main and sub programs, microcomputer program, file register comments, status latch, sampl-ing trace and extension file registers are not within the usable range of the CPU.	Read parameters in the CPU memory, check the contents, make	
		112		Total of the set capacity of the main and sub programs, file register comments, status latch, sampling trace and extension file registers exceeds capacity of the memory cassette.	necessary corrections and write them again to the memory.		
	11	113		STOP	STOP	Latch range set by parameters or setting of M, L or S is incorrect.	
		114		Sum check error Either of settings of the			
(Checked at		115		remote RUN/PAUSE contact point by parameters, operation mode at occurrence of error, annunciator indication mode, or STOP—— RUN indication mode is incorrect.	Read parameters in the CPU memory, check the contents, make necessary corrections and write them again to		
		116		The MNET-MINI automatic refresh setting by parameters is incorrect.	the memory		
power on and at STOP/		117		Timer setting by parameters is incorrect.			
PAUSE → RUN.)		118		Counter setting by parameters is incorrect.			

Table 6.3 Error Code List for A2ASCPU(S1/S30) and A2USHCPU-S1 (Continue)

Error Massage	Error Code (D9008)	Detailed Error Code (D9091)	CPU States	Error and Cause	Corrective Action							
"MISSING END INS"		121		The END END (FEND) instruction is not given in the main program.	Write the END instruction at the end of the main program.							
		122	STOP	The END (FEND) instruction is not given in the sub program if the sub program is set by parameters.								
	12	123		is set by a parameter, there is no END (FEND) instruction in subprogram 2. (2) When subprogram 2 is set by a parameter,	STOP	STOP	STOP	STOP	STOP	STOP	(FEND) instruction in subprogram 2. (2) When subprogram 2 is set by a parameter, subprogram 2 has not been written from a	Write the END instruction at the end of the sub
(Checked at STOP → RUN.)		124			program.							
"CAN'T EXECUTE (P)"	13	131	STOP	The same device number is used at two or more steps for the pointers (P) and interrupt pointers (I) used as labels to be specified at the head of jump destination.	Eliminate the same pointer numbers provided at the head of jump destination.							
(Checked at execution of instruction.)	13	132	STOP	Label of the pointer (P) specified in the the CJ, SCJ, CALL, CALLP, JMP, LEDAWBIFCALL or LEDAWBIREAK instruction is not provided before the END instruction.	Read the error step using a peripheral device, check contents and insert a jump destination pointer (P).							

Table 6.3 Error Code List for A2ASCPU(S1/S30) and A2USHCPU-S1 (Continue)

Error Massage	Error Code (D9008)	Detailed Error Code (D9091)	CPU States	Error and Cause	Corrective Action		
"CAN'T EXECUTE (P)"	13	133	STOP	(1) The RET instruction was included in the program and executed though the CALL instruction was not given. (2) The NEXT LEDAMBREAK instructions were included in the program and executed though the FOR instruction was not given. (3) Nesting level of the CALL, CALLP, and FOR instructions is 6 levels or deeper, and the 6th level was executed. (4) There is no RET or NEXT instruction at execution of the CALL or FOR instruction.	(1) Read the error step using a peripheral device, check contents and correct program of the step. (2) Reduce the number of nesting levels of the CALL, CALLP and FOR instructions to 5 or less.		
	134	134				The CHG instruction was included in the program and executed though no sub program was provided.	Read the error step using a peripheral device and delete the CHG instruction circuit block.
(Checked at execution of instruction.)		135		(1) LEDA/B IX and LEDA/B IXEND instructions are not paired. (2) There are 33 or more sets of LEDA/B IX and LEDA/B IXEND instructions.	(1) Read the error step using a peripheral device, check contents and correct program of the step. (2) Reduce the number of sets of LEDA/BIXI and LEDA/BIXEND instructions to 32 or less.		

Table 6.3 Error Code List for A2ASCPU(S1/S30) and A2USHCPU-S1 (Continue)

Error Massage	Error Code (D9008)	Detailed Error Code (D9091)	CPU States	Error and Cause	Corrective Action
"CHK FORMAT ERR"		141		Instructions (including NOP) other than LDX, LDIX, ANDX and ANIX are included in the CHK instruction circuit block.	
		142		Multiple CHK instructions are given.	
	143 144 14 145		The number of contact points in the CHK instruction circuit block exceeds 150.		
		144	STOP	STOP are not paired with the LEDAICHKEND instructions, or 2 or more pairs of them are given are given	Check the program of the CHK instruction and correct it referring to contents of
		145		Format of the block shown below, which is provided before the CHK instruction circuit block, is not as specified.	detailed error codes.
(Checked at		146		Device number of D1 in the CHKD1D2 instruction is different from that of the contact point before the CJPC instruction.	
STOP/ PAUSE → RUN.)		147		Index qualification is used in the check pattern circuit.	

Table 6.3 Error Code List for A2ASCPU(S1/S30) and A2USHCPU-S1 (Continue)

Error Massage	Error Code (D9008)	Detailed Error Code (D9091)	CPU States	Error and Cause	Corrective Action
"CHK FORMAT ERR" (Checked at STOP/ PAUSE → RUN.)	14	148	STOP	(1) Multiple check pattern circuits of the LEDACHKE - LEDACHKE-ND instructions are given. (2) There are 7 or more check condition circuits in the LEDACHKE-ND instructions. (3) The check condition circuits in the LEDACHKE-ND instructions circuits in the LEDACHKE-ND instructions are written without using X and Y contact instructions or compare instructions. (4) The check pattern circuits of the LEDACHKE-ND instructions are written with 257 or more steps.	Check the program of the CHK instruction and correct it referring to contents of detailed error codes.

Table 6.3 Error Code List for A2ASCPU(S1/S30) and A2USHCPU-S1 (Continue)

Error Massage	Error Code (D9008)	Detailed Error Code (D9091)	CPU States	Error and Cause	Corrective Action
"CAN'T EXECUTE (I)"		151		The IRET instruction was given outside of the interrupt program and was executed.	Read the error step using a peripheral device and delete the IRET instruction.
	152		There is no IRET instruction in the interrupt program.	Check the interrupt program if the <code>IRET</code> instruction is given in it. Write the <code>IRET</code> instruction if it is not given.	
(Checked at occurrence of interrupt.)	15	153	STOP	Though an interrupt module is used, no interrupt pointer (I) which corresponds to the module is given in the program. Upon occurrence of error, the problem pointer (I) number is stored at D9011.	Monitor special register D9011 using a peripheral device, and check if the interrupt program that corresponds to the stored data is provided or if two or more interrupt pointers (I) of the same number are given. Make necessary corrections.
"CASSETTE ERROR"	16	1	STOP	Memory cassette is not loaded.	Turn off the PC power and load the memory cassette.
"RAM ERROR"		201		The sequence program storage RAM in the CPU module caused an error.	
	20	202	STOP	The work area RAM in the CPU module caused an error.	Since this is CPU hardware error,
	20	203	3101	The device memory in the CPU module caused an error.	consult Mitsubishi representative.
(Checked at power on.)		204		The address RAM in the CPU module caused an error.	

Table 6.3 Error Code List for A2ASCPU(S1/S30) and A2USHCPU-S1 (Continue)

Error Massage	Error Code (D9008)	Detailed Error Code (D9091)	CPU States	Error and Cause	Corrective Action
"OPE CIRCUIT ERROR"		211		The operation circuit for index qualification in the CPU does not work correctly.	
		212		Hardware (logic) in the CPU does not operate correctly.	
(Checked at power on.)	21	213	STOP	The operation circuit for sequential processing in the CPU does not operate correctly.	Since this is CPU hardware error, consult Mitsubishi
"OPE. CIRCUIT ERR."		214		In the END processing check, the operation circuit for index qualification in the CPU does not work correctly.	representative.
(Checked at execution of the END instruction)		215		In the END processing check, the hardware in the CPU does not operate correctly.	
"WDT ERROR" (Checked at execution of END processing.)	22		STOP	Scan time is longer than the WDT time. (1) Scan time of the user's program has been extended due to certain conditions. (2) Scan time has been extended due to momentary power failure occurred during scanning.	(1) Calculate and check the scan time of user program and reduce the scan time using the CJ instruction or the like. (2) Monitor contents of special register D9005 using a peripheral device. If the contents are other than 0, power supply voltage may not be stable. Check power supply and reduce variation in voltage.

Table 6.3 Error Code List for A2ASCPU(S1/S30) and A2USHCPU-S1 (Continue)

Error Massage	Error Code (D9008)	Detailed Error Code (D9091)	CPU States	Error and Cause	Corrective Action
"END NOT EXECUTE" (Checked at execution of the END instruction.)	24	241	STOP	Whole program of specified program capacity was executed without executing the END instructions. (1) When the END instruction was to be executed, the instruction was read as other instruction code due to noise. (2) The END instruction changed to other instruction code due to unknown cause.	(1) Reset and run the CPU again. If the same error recurs, Since this is CPU hardware error, consult Mitsubishi representative.
"MAIN CPU DOWN"	26	_	STOP	The main CPU is malfunctioning or faulty.	Since this is CPU hardware error, consult Mitsubishi representative
"UNIT VERIFY ERR" (Checked continuously.)	31	_	Stop or Continue (set by para- meter)	Current I/O module information is different from that recognized when the power was turned on. (1) The I/O module (including special function modules) connection became loose or the module was disconnected during operation, or wrong module was connected.	Read detailed error code using a peripheral device and check or replace the module which corresponds to the data (I/O head number). Or, monitor special registers D9116 to D9123 using a peripheral device and check or replace the modules if corresponding data bit is "1".

Table 6.3 Error Code List for A2ASCPU(S1/S30) and A2USHCPU-S1 (Continue)

Error Massage	Error Code (D9008)	Detailed Error Code (D9091)	CPU States	Error and Cause	Corrective Action
"FUSE BREAK OFF"	32	_	Stop or Continue (set by para- meter)	(1) There is an output module of which fuse is blown. (2) The external power supply for output load is turned OFF or is not connected.	(1) Check the FUSE BLOWN indicator LED on the output module and replace the fuse. (2) Read detailed error code using a peripheral device and replace the fuse of the output module which corresponds to the data (I/O head number). Or, monitor special registers D9100 to D9107 using a peripheral device and replace the fuse of the output module of which corresponding data bit is "1". (3) Check the ON/OFF status of the external power supply for output load.

Table 6.3 Error Code List for A2ASCPU(S1/S30) and A2USHCPU-S1 (Continue)

Error Massage	Error Code (D9008)	Detailed Error Code (D9091)	CPU States	Error and Cause	Corrective Action	
		401		Due to the error of the control bus which connects to special function modules, the FROM/TO instruction cannot be executed.	Since it is a hardware error of special function	
"CONTROL- BUS ERR"	40	402	STOP	If parameter I/O assignment is being executed, special function modules are not accessible at initial communication. At error occurrence, the head I/O number (upper 2 digits of 3 digits) of the special function module that caused error is stored at D9011.	module, CPU module or base module, replace and check defective module(s). Consult Mitsubishi representative for defective modules.	
		411	411		Though an access was made to a special function module at execution of the FROM/TO instruction no response is received.	
"SP.UNIT DOWN"	41	412	STOP	If parameter I/O assignment is being executed, no response is received from a special function module at initial communication. At error occurrence, the head I/O number (upper 2 digits of 3 digits) of the special function module that caused error is stored at D9011.	Since it is hardware error of the special function module to which an access was made, consult Mitsubishi representative.	
"LINK UNIT ERROR"	42	_	STOP	(1) Either data link module is loaded to the master station. (2) There are 2 link modules which are set to the master station (station 0).	(1) Remove data link module from the master station. (2) Reduce the number of master stations to 1. Reduce the link modules to 1 when the 3-tier system is not used.	

Table 6.3 Error Code List for A2ASCPU(S1/S30) and A2USHCPU-S1 (Continue)

Error Massage	Error Code (D9008)	Detailed Error Code (D9091)	CPU States	Error and Cause	Corrective Action
"I/O INT. ERROR"	43	_	STOP	Though the interrupt module is not loaded, an interrupt occurred.	Since it is hardware error of a module, replace and check adefective module. For defective modules, consult Mitsubishi representative.
		441		A special function module is assigned as an I/O module, or vice versa, in the I/O assignment using parameters from the peripheral device.	Execute I/O assignment again using parameters from the peripheral device according to the loading status of special function modules.
		442		There are 9 or more special function modules (except the interrupt module) which can execute interruption to the CPU module loaded.	Reduce the special function modules (except the interrupt module) which can execute interrupt start to 8 or less.
"SP.UNIT LAY.ERR."	44	443	STOP	There are 2 or more data link modules loaded.	Reduce the data link modules to 1 or less.
LAT.ERK.		444		There are 7 or more modules such as a computer link module loaded to one CPU module.	Reduce the computer link modules to 6 or less.
		445		There are 2 or more interrupt modules loaded.	Reduce the interrupt modules to 1 or less.
		446		Modules assigned by parameters for MNT/MINI automatic refresh from the peripheral device do not conform with the types of station modules actually linked.	Perform again module assignment for MNT/MINI automatic refresh with parameters according to actually linked station modules.

Table 6.3 Error Code List for A2ASCPU(S1/S30) and A2USHCPU-S1 (Continue)

Error Massage	Error Code (D9008)	Detailed Error Code (D9091)	CPU States	Error and Cause	Corrective Action
"SP.UNIT LAY.ERR."	44	447	STOP	The number of modules of I/O assignment registration (number of loaded modules) per one CPU module for the special function modules which can use dedicated instructions is larger than the specified limit. (Total of the number of computers shown below is larger than 1344.) The number of modules of I/O assignment registration (number of loaded modules) per one CPU module for the special function modules which can use dedicated instructions is larger than the specified limit. (Total of the number of computers shown below is larger than 1344.) (AD59×5) (AD57(S1)/AD58×8) (AJ71C24(S3/S6/S8)×10) (AJ71C24(S1)(S2)×29) + ((AJ71C21(S1)(S2)×29) + ((AJ71PT32(S3)) in extension mode ×125)	Reduce the number of loaded special function modules.
	448*		 (1) Five or more network modules have been installed. (2) A total of five or more of network modules and data link modules have been installed. 	Make the total of the installed network modules and data link modules four or less.	

Table 6.3 Error Code List for A2ASCPU(S1/S30) and A2USHCPU-S1 (Continue)

Error Massage	Error Code (D9008)	Detailed Error Code (D9091)	CPU States	Error and Cause	Corrective Action
"SP.UNIT ERROR"		461		Module specified by the FROM/TO instruction is not a special function module.	Read the error step using a peripheral device and check and correct contents of the FROM/TO instruction of the step.
(Checked at execution of the FROM/TO instruction or the dedicated instructions for special function modules.)	46	462	Stop or Continue (set by para- meter)	(1) Module specified by the dedicated instruction for special function module is not a special function module or not a corresponding special function module. (2) A command was issued to a CC-Link module with function version under B. (3) A CC-Link dedicated command was issued to a CC-Link module for which the network parameters have not been set.	(1) Read the error step using a peripheral device and check and correct contents of the dedicated instruction for special function modules of the step. (2) Replace with a CC-Link module having function version B and above. (3) Set the parameters.

Table 6.3 Error Code List for A2ASCPU(S1/S30) and A2USHCPU-S1 (Continue)

Error Massage	Error Code (D9008)	Detailed Error Code (D9091)	CPU States	Error and Cause	Corrective Action
"LINK PARA. ERROR"	47	0	Continue	When using MELSECNET/(II) (1) When the link range at a data link CPU which is also a master station (station number = 00) is set by parameter settling at a peripheral device, for some reason the data written to the link parameter area differs from the link parameter data read by the CPU. Alternatively, no link parameters have been written. (2) The total number of slave stations is set at 0. (3) The head I/O number of the network parameters is incorrect.	(1) Write the parameters again and check. (2) Check the station number settings. (3) Check the head I/O number of the network parameters. (4) Persistent error occurrence may indicate a hardware fault. Consult your nearest Mitsubishi representative, explaining the nature of the problem.

Table 6.3 Error Code List for A2ASCPU(S1/S30) and A2USHCPU-S1 (Continue)

Error Massage	Error Code (D9008)	Detailed Error Code (D9091)	CPU States	Error and Cause	Corrective Action
"LINK PARA. ERROR"	47	471*	Continue	[When using MELSECNET/10] (1) The transfer source device range and transfer destination device range specified for the inter-network transfer parameters are in the same network. (2) The specified range of transfer source devices or transfer destination devices for the inter-network transfer parameters spans two or more networks. (3) The specified range of transfer source devices or transfer destination devices for the inter-network transfer parameters spans two or more networks.	Write the network parameters again and check.
	472*		[When using MELSECNET/10] The contents of the routing parameters written from a peripheral device differ from the actual network system.	Write the routing parameters again and check.	

Table 6.3 Error Code List for A2ASCPU(S1/S30) and A2USHCPU-S1 (Continue)

Error Massage	Error Code (D9008)	Detailed Error Code (D9091)	CPU States	Error and Cause	Corrective Action
"LINK PARA.	473* 'LINK PARA. 47 Col	Continue	[When using MELSECNET/10] (1) The contents of the network parameters for the first link unit, written from a peripheral device, differ from the actual network system. (2) The link parameters for the first link unit have not been written. (3) The setting for the total number of stations is 0.	(1) Write the parameters again and check. (2) Check the station number settings. (3) Persistent error occurrence may indicate a hardware fault. Consult your nearest Mitsubishi representative, explaining the nature of the problem.	
ERROR"	71	474*	Continue	[When using MELSECNET/10] (1) The contents of the network parameters for the second link unit, written from a peripheral device, differ from the actual network system. (2) The link parameters for the second link unit have not been written. (3) The setting for the total number of stations is 0.	Write the parameters again and check. Check the station number settings. Persistent error occurrence may indicate a hardware fault. Consult your nearest Mitsubishi representative, explaining the nature of the problem.

Table 6.3 Error Code List for A2ASCPU(S1/S30) and A2USHCPU-S1 (Continue)

Error Massage	Error Code (D9008)	Detailed Error Code	CPU States	Error and Cause	Corrective Action
		(D9091) 475*		[When using MELSECNET/10] (1) The contents of the network parameters for the third link unit, written from a peripheral device, differ from the actual network system. (2) The link parameters for the third link unit have not been written. (3) The setting for the total number of stations is 0.	
"LINK PARA. ERROR"	47	476*	Continue	[When using MELSECNET/10] (1) The contents of the network parameters for the fourth link unit, written from a peripheral device, differ from the actual network system. (2) The link parameters for the fourth link unit have not been written. (3) The setting for the total number of stations is 0.	(1) Write the parameters again and check. (2) Check the station number settings. (3) Persistent error occurrence may indicate a hardware fault. Consult your nearest Mitsubishi representative, explaining the nature of the problem.
	477	A ink parameter error was detected by the CC-Link module.	(1) Write the parameters in again and check. (2) If the error appears again, there is a problem with the hardware. Consult your nearest System Service, sales office or branch office.		

Table 6.3 Error Code List for A2ASCPU(S1/S30) and A2USHCPU-S1 (Continue)

Error Massage	Error Code (D9008)	Detailed Error Code (D9091)	CPU States	Error and Cause	Corrective Action
"OPERATION ERROR"		501		(1) When file registers (R) are used, operation is executed outside of specified ranges of device numbers and block numbers of file registers (R). (2) File registers are used in the program without setting capacity of file registers.	Read the error step using a peripheral device and check and correct
		502		Combination of the devices specified by instruction is incorrect.	program of the step.
		503		Stored data or constant of specified device is not in the usable range.	
	50	504	Stop or Continue (set by para-	Set number of data to be handled is out of the usable range.	
		505	meter)	(1) Station number specified by the LEDA/BLRDP LEDA/BLWTP, LRDP, LWTP instructions is not a local station. (2) Head I/O number specified by the LEDA/BRTDP, RFRP, RTOP instructions is not of a remote station.	Read the error step using a peripheral device and check and correct program of the step.
(Checked at execution of instruction.)		506		Head I/O number specified by the LEDA/BRTRP LEDA/BRTOP, RTRP, RTOP instructions is not of a special function module.	

Table 6.3 Error Code List for A2ASCPU(S1/S30) and A2USHCPU-S1 (Continue)

Error Massage	Error Code (D9008)	Detailed Error Code (D9091)	CPU States	Error and Cause	Corrective Action
"OPERATION ERROR"	50	507	Stop or Continue (set by para- meter)	(1) When the AD57(S1) or AD58 was executing instructions in divided processing mode, other instructions were executed to either of them. (2) When an AD57(S1) or AD58 was executing instructions in divided processing mode, other instructions were executed in divided mode to another AD57(S1) or AD58.	Read the error step using a peripheral device and provide interlock with special relay M9066 or modify program structure so that, when the AD57(S1) or AD58 is executing instructions in divided processing mode, other instructions may not be executed to either of them or to another AD57(S1) or AD58 in divided mode.
(Checked at execution of instruction.)		508		A CC-Link dedicated command was issued to three or more CC-Link modules.	The CC-Link dedicated command can be issued only to two or less CC-Link modules.

Table 6.3 Error Code List for A2ASCPU(S1/S30) and A2USHCPU-S1 (Continue)

Error Massage	Error Code (D9008)	Detailed Error Code (D9091)	CPU States	Error and Cause Corrective Action
"OPERATION ERROR" (Checked at execution of instruction.)	50	509	STOP	(1) An instruction which cannot be executed by remote terminal modules connected to the MNET/MINI-S3 was executed to the modules. (2) Though there are 32 entries of FROM or TO instruction in the mailbox memory area waiting for execution), another PRC instruction is executed to cause an overflow in the mail box (memory area waiting for execution). (3) The PIDCONT instruction was executed without executing the PIDINIT or PIDCONT instruction. The PIDST instruction. The program presently executed was specified by the ZCHG instruction. (4) The number of CC-Link dedicated command executed in one scan exceeded 10.

Table 6.3 Error Code List for A2ASCPU(S1/S30) and A2USHCPU-S1 (Continue)

Error Massage	Error Code (D9008)	Detailed Error Code (D9091)	CPU States	Error and Cause Corrective Action
	60	_		(1) The CPU countermeasu res for noise. (2) Hardware failure. (3) The CPU countermeasu res for noise. (4) Since this is hardware error, consult Mitsubishi representative.
"MAIN CPU DOWN"	62	_	STOP	(1) The power supply module detected an incorrect power waveform. (2) Failure in the power module, CPU module, main base unit or expansion cable is detected. (1) Correct the power waveform applied to the power supply module. (2) Replace the power module, CPU module, main base unit or expansion cable.
"BATTERY ERROR" (Checked at power on.)	70	_	Continue	(1) Battery voltage has lowered below specified level. (2) Battery lead connected. (1) Replace battery. (2) If a RAM memory or specified level. (2) Eattery lead connector is not connected. (1) Replace battery. (1) If a RAM memory or power failure compensation function is used, connect the lead connector.

APPENDICES

Appendix 1 CPU-by-CPU Startup Names

CPU	Startup Name	CPU Name to Be Selected If the CPU Name Given on the Left Is Not Found (Priority of selection : 1 → 2 → 3)
A1SHCPU	A1SH	A3
A2SHCPU(S1)	A2SH	A3
A1SCPUC24-R2	A1S	1 2 A0J2H → A2
A2ASCPU (S1)	A2US	$\begin{array}{cccccccccccccccccccccccccccccccccccc$
A2ASCPU-S30	A2US	1 2 3 A3U → A3A → A3H

Appendix 2 Transportation Precautions

When transporting lithium batteries, make sure to treat them based on the transportation regulations.

Appendix 2.1 Relevant Models

The battery for A1SHCPU, A2SHCPU(S1), A1SCPUC24-R2, A2ASCPU(S1), A2ASCPU-S30 and A2USHCPU-S1 is classified as follows:

Product Name	Model	Description	Handled as
A series battery	A6BAT	Lithium battery	Non-dangerous goods

Appendix 2.2 Transportation Guidelines

Products are packed properly in compliance with the transportation regulations prior to shipment. When repacking any of the unpacked products to transport it to another location, make sure to observe the IATA Dangerous Goods Regulations, IMDG Code and other local transportation regulations.

For details, please consult your transportation company.

MEMO

WARRANTY

Please confirm the following product warranty details before using this product.

1. Gratis Warranty Term and Gratis Warranty Range

If any faults or defects (hereinafter "Failure") found to be the responsibility of Mitsubishi occurs during use of the product within the gratis warranty term, the product shall be repaired at no cost via the sales representative or Mitsubishi Service Company.

However, if repairs are required onsite at domestic or overseas location, expenses to send an engineer will be solely at the customer's discretion. Mitsubishi shall not be held responsible for any re-commissioning, maintenance, or testing on-site that involves replacement of the failed module

[Gratis Warranty Term]

The gratis warranty term of the product shall be for one year after the date of purchase or delivery to a designated place.

Note that after manufacture and shipment from Mitsubishi, the maximum distribution period shall be six (6) months, and the longest gratis warranty term after manufacturing shall be eighteen (18) months. The gratis warranty term of repair parts shall not exceed the gratis warranty term before repairs. [Gratis Warranty Range]

- (1)The range shall be limited to normal use within the usage state, usage methods and usage environment, etc., which follow the conditions and precautions, etc., given in the instruction manual, user's manual and caution labels on the product.
- (2)Even within the gratis warranty term, repairs shall be charged for in the following cases.
 - Failure occurring from inappropriate storage or handling, carelessness or negligence by the user. Failure caused by the user's hardware or software design.
 - Failure caused by unapproved modifications, etc., to the product by the user.
 - 3.When the Mitsubishi product is assembled into a user's device, Failure that could have been avoided if functions or structures, judged as necessary in the legal safety measures the user's device is subject to or as necessary by industry standards, had been provided.
 - 4.Failure that could have been avoided if consumable parts (battery, backlight, fuse, etc.) designated in the instruction manual had been correctly serviced or replaced.
 - 5.Failure caused by external irresistible forces such as fires or abnormal voltages, and Failure caused by force majeure such as earthquakes, lightning, wind and water damage.
 - Failure caused by reasons unpredictable by scientific technology standards at time of shipment from Mitsubishi.
 - 7.Any other failure found not to be the responsibility of Mitsubishi or that admitted not to be so by the user.

2. Onerous repair term after discontinuation of production

- (1)Mitsubishi shall accept onerous product repairs for seven (7) years after production of the product is discontinued. Discontinuation of production shall be notified with Mitsubishi Technical Bulletins. etc.
- (2)Product supply (including repair parts) is not available after production is discontinued

3. Overseas service

Overseas, repairs shall be accepted by Mitsubishi's local overseas FA Center. Note that the repair conditions at each FA Center may differ.

4. Exclusion of loss in opportunity and secondary loss from warranty liability

Regardless of the gratis warranty term, Mitsubishi shall not be liable for compensation of damages caused by any cause found not to be the responsibility of Mitsubishi, loss in opportunity, lost profits incurred to the user by Failures of Mitsubishi products, special damages and secondary damages whether foreseeable or not, compensation for accidents, and compensation for damages to products other than Mitsubishi products, replacement by the user, maintenance of on-site equipment, start-up test run and other tasks.

5. Changes in product specifications

The specifications given in the catalogs, manuals or technical documents are subject to change without prior notice.

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